

EEL 3701C: DIGITAL LOGIC AND COMPUTER SYSTEMS

<http://mil.ufl.edu/3701/>

INSTRUCTORS

Dr. Eric M. Schwartz MAEB 321 392-2541 ems@mil.ufl.edu Office Hours: Wed, period 6; Fri, period 8

LECTURES

Tues 4th & Thur 4th-5th (10:40-11:30am, 10:40am-12:35pm) in CHE 237
 Tues 7th & Thur 7th-8th (1:55-2:45pm, 1:55-3:50pm) in LAR 330

LAB SECTIONS

(NEB 248)

1488 M 9-11 (JW)	9069 M E1-3 (LD)	1496 Tu 9-11 (JW)	9070 T E1-3 (LD)
05F7 W 3-5 (MR)	1490 W 9-11 (JW)	1491 W E1-3 (MT)	5297 Th 6-8 (GD)
5089 Th 9-11 (GD)	5712 Th E1-3 (GD)		

REQUIRED TEXTBOOK (Share, Borrow, Buy, or Rent one of the below. See website for more info)

Charles H. Roth Jr., *Fundamentals of Logic Design, 6th edition*, Cengage Learning, Stamford, Connecticut, 2009. ISBN: 0495471690

Charles H. Roth Jr., *Fundamentals of Logic Design, 5th edition*, Thomson Brooks/Cole Publishing, Belmont, California, 2004. ISBN: 0534378048

RECOMMENDED REFERENCE TEXTBOOK

Reprinted Chapters 1-7 from H. Lam, and J. O'Malley, *Fundamentals of Computer Engineering: Logic Design and Microprocessors, 1st edition*, 1988, John Wiley and Sons, New York, available at the Target Copy, 1412 W. University Ave., Gainesville, FL; Phone: 352-376-3826. <http://target-copy.com> [\$18.26]

REQUIRED HARDWARE

A special USB Blaster programming cable (from Altera) is required for this course. The price for this cable is \$50 plus tax. The cable is available at the UF bookstore; go to the counter in the computer area to request one. This device is also used in two other courses (EEL 4744 and EEL 4712). More information about this required device will be provided in class. You will also need your **own laptop PC** for use in lab and possibly during the exams.

COURSE OBJECTIVES (ABET Design Content 50%) [Lab fee: \$91.55]

To learn to: perform elementary manipulations of Boolean algebraic equations; simplify logic expressions; design combinational and sequential circuits; use a digital design and simulation package, use a hardware description language (HDL), analyze binary storage device behavior and applications, and to study the fundamentals of microprocessor architecture, including assembly language programming and the design of basic components of a microprocessor.

CLASS ATTENDANCE AND BEHAVIOR

Class attendance is not mandatory, but all classes are important. Missing a class may be hazardous to your grade. There will be pop (unannounced) quizzes. A missed quiz cannot be made up. (See *Course Requirements* below for policy on missed quizzes.)

Turn off all cell phones, beepers, laptop sound effects, and other noise making devices **before entering** our classroom. If a noise-making device goes off during class, I reserve the right to **lower your course grade**. If a noise-making device goes off during an exam, you will lose a significant number of points on this exam.

TA OFFICE HOURS

You may go to any TA available (in NEB 248, not just the one teaching your lab section. The instructor will hold office hours (posted above and on our web page) or by appointment. You are encouraged to use e-mail to communicate with the instructors and TAs. A TA will also hold evening help sessions on **Monday (Gautam) and Wednesday (Josh), at 7:20pm in MAEB, 2nd floor (room TBD).**

TA name	Josh Weaver	Gautam Dash	Matt Thompson	Logan Dunberg	Marisha Rawlins
office hours	M & W: 7 th ; Help T: E1	Tu: 5 th -6 th ; Help M: E1	M: 8 th	M & F: 6 th	Tu: 3 rd
e-mail	josh.n.weaver@ufl.edu	gbdash@ufl.edu	matthewbot@ufl.edu	ldunberg@ufl.edu	mrawlins@ufl.edu

Exam Schedule

EXAM SCHEDULE

The exams will be given outside of class time, in the evening.

EXAM	DATE	TIME	LOCATION
Exam 1	Mon, 3 Oct	5:10 pm	NEB 202
Exam 2	Wed, 9 Nov	5:10 pm	CSE E119
Exam 3	Tues, 6 Dec	5:10 pm	

COURSE REQUIREMENTS (IMPORTANT!!!)

- Perform all laboratory experiments. A grade of 65% or better in Lab is **required** in order to obtain a passing grade. Your lowest lab (**not including lab 8**) will be dropped. But use this drop wisely, i.e., do **not** just skip a lab since all labs are important and your next missed lab may be unavoidable. If you need to miss a single lab, it's ok; you can **not** make up the missed lab. (You should do this lab on your own.) **If you have a valid reason for missing this lab, get documentation for your first missed lab and hold on to it.** If you miss a **second** lab, you must show the **professor** (not the TA) **written documentation for BOTH your first and your second missed labs.** This documentation should be official and from a doctor, judge, etc., so that a make-up can be arranged. You must notify the professor **prior** to your scheduled second missed lab or **as soon as possible after** your second missed lab.
 - Labs **must** be done at scheduled times.
 - An average lab grade of **65% or higher** is required to be **eligible** to **pass** the class!
- 1. Do all homework assignments and turn them in **within the first 3 minutes of class.**
 - **Late homework will not be accepted.**
- 2. A quiz can happen at any time, during any class, i.e., quizzes are generally not announced ahead of time. You should therefore not miss class.
 - **Missed quizzes cannot be made up.**
 - **The same policy for missed labs (as described above) applies to missed quizzes.**
- 3. Take **3** during-term exams. (Note that there is **NO** final exam at the scheduled time.)
 - **No make-up exams will be given except for a medically documented incapacity or family emergency.**

STUDENTS WITH DISABILITIES

Students requesting classroom, laboratory or exam accommodations must first register with the Dean of Students Office. The Dean of Students Office will provide documentation to the student who must then provide this documentation to the instructor when requesting accommodation.

UF COUNSELING SERVICES

Resources are available on-campus for students having personal problems or lacking clear career and academic goals. The resources include:

- University Counseling Center, 301 Peabody Hall, 392-1575, Personal and Career Counseling.
- SHCC mental Health, Student Health Care Center, 392-1171, Personal and Counseling.
- Center for Sexual Assault/Abuse Recovery and Education (CARE), Student Health Care Center, 392-1161, sexual assault counseling.
- Career Resource Center, Reitz Union, 392-1601, career development assistance and counseling.

STUDENT PRIVACY (more on last page)

There are federal laws protecting your privacy with regards to grades earned in courses and on individual assignments. Please see the last page of this document for more information.

SOFTWARE USE

All faculty, staff and student of the University are required and expected to obey the laws and legal agreements governing software use. Failure to do so can lead to monetary damages and/or criminal penalties for the individual violator. Because such violations are also against University policies and rules, disciplinary action will be taken as appropriate. We, the members of the University of Florida community, pledge to uphold ourselves and our peers to the highest standards of honesty and integrity.

HOMEWORK AND EXAM SOLUTIONS

Solutions to homework will be made available on our class web site. Practice exams (some with solutions) are also posted.

COURSE GRADE DETERMINATION

Midterm Exams (2@20%)	40%	
Laboratory	30%*	(Lab values vary, i.e. it could count as 1/3 a lab, a single lab, a double lab, etc.)
Homework/Quizzes	4%	(5-10 homework and 0-5 quizzes)
<u>End-term Exam</u>	<u>26%</u>	(comprehensive; 90 or better results in 5% grade bonus, e.g., 86% ⇒ 91%)
Total	100%	

All grades are **non-negotiable one week** after the grade is posted. Please don't come to me after the final grades have been posted with a hard-luck story.

* A grade of 65% or better in Lab is **required** in order to obtain a passing grade. Your lowest lab (**not including lab 8**) will be dropped. But use this drop wisely, i.e., do **not** just skip a lab since all labs are important and your next missed lab may be unavoidable. If you need to miss a single lab, it's ok; you can **not** make up the missed lab. (You should do this lab on your own.) **If you have a valid reason for missing this lab, get documentation for your first missed lab and hold on to it.** If you miss a **second** lab, you must show the **professor** (not the TA) **written documentation for BOTH your first and your second missed labs.** This documentation should be official, i.e., from a doctor, judge, etc., so that a make-up can be arranged. You must notify the professor **prior** to your scheduled second missed lab or **as soon as possible after** your second missed lab.

Note: All grading percentages are subject to change at professor's discretion. Students will be notified of any changes.

GRADING POLICY

UF grades are often distributed according to the following **rough** distribution:

A: 10% B: 35% C: 45% D&E: 10%

This usually works out to mean that if you make class average you will earn close to a “C+” or “B-”. If you score 10 percent above the class average, you will probably earn a “A.” **This is not a contract on grading.** Rather, this information serves to provide you a rough understanding of your academic standing at any time during the semester. Grades are periodically posted on the class web site. **It is your responsibility to check your grades regularly** since mistakes often happen when dealing with a large number of students and TAs. **All grades are final one week after posting.** After curving exams as needed, course grades are assigned using the 70 (C), 80 (B), and 90 (A) cuts. [$86.\bar{6} \rightarrow 89.\bar{9}$ (A-), $83.\bar{3} \rightarrow 86.\bar{6}$ (B+)]

All grades are **non-negotiable one week** after the grade is posted. Please don't come to me after the final grades have been posted with a hard-luck story.

The UF grading policies for assigning grade points can be found on the following undergraduate catalog web page: <https://catalog.ufl.edu/ugrad/current/regulations/info/grades.aspx>.

Part of your grade on exams, labs, homework, quizzes, etc. is based not only on solving the problem you are presented with, but the manner in which you solve it. For example, there is a difference between two designs that meet the given specifications, but one is an elegant, modular 3-element solution, while the other is an obfuscated 5-element design that also meets the specifications but would be difficult to extend later. Just as your future employer would value the latter design less than the first, so will I in grading your assignments.

HOMEWORK GRADING

Homework is due in the classroom at the **start** of any of our class periods. When homework is returned, students should compare their solutions to the posted solutions since homework grading is only cursory. **Late homework will not be accepted.**

IN-CLASS QUIZ GRADING

In-class quizzes will cover material previously covered in assigned readings, homework, class or lab. Quizzes may happen during any class; they are not generally announced beforehand. **Missed quizzes can not be made up, but your lowest quiz (or homework) is dropped.** Therefore, missing a single quiz will not hurt your grade. See the Course Requirements above for the policy for missed quizzes.

DROPPING AND BRIGHT FUTURES

Several new policies were implemented with Bright Futures in Fall 2009. Students must **refund** the cost of any dropped or withdrawn Bright Futures funded course. Full-time students must earn 24 semester hours per academic year. If you drop a course, you still must earn 24 semester hours per academic year in order to continue with Bright Futures funding. For more information, please contact the UF Student Financial Affairs office (<http://www.sfa.ufl.edu/programs/state-of-florida-programs/florida-bright-futures-scholarship-program/>), phone: 352-392-1275).

MULTIMEDIA CLASS/AUDIENCE NOTES

Audience notes are normally available from the class web site every week or so for the subsequent week or more of classes. The notes consist of pdf versions of the class PowerPoint slides with some space for note taking. These notes are not required but are **highly** recommended. Check the class web site for information on exactly when the notes are available.

For optimal performance, read the notes and examples for a class **before** that class and bring the **printed class notes and examples** to class to augment the printed material with your own notes. Notes will be removed shortly after they are covered in class.

EXAM RE-GRADE POLICY

If you believe an error has been made on an exam score you must make a **written** request to the instructor explaining where the misgrading or error occurred. This request must be submitted **immediately at the end of the class in which the exam is returned.** If you do resubmit an exam, however, the instructor reserves the right to scrutinize and grade the **entire** exam more closely. This definitely places your current score at risk. Consequently, it is not advisable to resubmit an exam for correction unless a blatant error, such as a miscalculation of total points, has been made. You **must** make it clear what writing you added to the exam (by clear indication, e.g., use a different color pen or pencil) after it was returned to you.

ACADEMIC HONESTY

All students admitted to the University of Florida have signed a statement of academic honesty committing them to be honest in all academic work and understanding that failure to comply with this commitment will result in disciplinary action.

This statement is a reminder to uphold your obligation as a student at the University of Florida and to be honest in all work submitted and exams taken in this class and all others.

CHEATING

CHEATING WILL NOT BE TOLERATED. We will actively search for cheaters. If you are caught, there will be no negotiations. You will fail the course and get reported to the honor court. There are **no excuses and no exceptions**. You may talk to other students about homework and lab assignments, but the final work **must** be your own. If you are caught cheating on **any** assignment (homework, lab, quiz or exam), the **smallest** penalty possible is failure of the course. During a recent semester many students were caught with partly copied lab assignments. If this happens this semester, all of the guilty students will earn an “E” in the course. A meeting with the instructor will determine **additional penalties**, none of which are desirable or pleasant (*i.e.*, cheating in this course will result in a failing grade in the course, initiation of honor court charges, and possibly expulsion from the university). If you know someone is cheating, **it is your responsibility to report it**. We have and will continue to prosecute cheaters by turning them over to the office of Student Judicial Affairs. For more information about cheating, see the URLs: <http://www.dso.ufl.edu/sccr/honorcodes/conductcode.php> . For the copy of the UF Honor Code and consequences of academic dishonesty, please refer to <http://www.dso.ufl.edu/sccr/honorcodes/honorcode.php>.

WORKING TOGETHER <http://www.dso.ufl.edu/sccr/faculty/process.php><http://www.dso.ufl.edu/sccr/webforms/incidentreport.php> .

You are encouraged to work with other students on **homework** assignments, but in a professional manner.

You must do independent work on labs. Although you may **consult** with other students, TAs, or Professors, you **must** do independent work. Consulting means “**seeking opinions or advice**” **not** getting working programs or designs, understanding them, and then modifying them to make them your own. The latter constitutes cheating (see above section). Working side-by-side to construct a program or design in a group constitutes cheating. (Solving labs are good practice for solving quizzes and exams, which are also **not** group activities.)

LABORATORY GRADING

You will not be admitted to the lab without a prelab, *i.e.*, a printout of **ALL** your circuit diagrams, simulations, programs (VHDL or assembly), and answers to pre-lab questions. **Quartus archive (.qar) file(s) (containing all of the associated design and simulation files)** must **ALSO** be **emailed** to eel3701uf@gmail.com **BEFORE** the start of your lab. (Do **NOT** send the email to your TA or the instructor.) **Proper subject headings are required.** You will receive a confirmation email; if you do **NOT** receive the confirmation email, then your email did **NOT** get through. Check your the email address and **send it again**.

The subject should have the following information: First Last Section# Lab#, with each field separated. For example, Tebow Tim 1515 LAB1, is a possible subject line. There should be **NO OTHER characters**, in the subject.. Each circuit diagram, HDL file, and assembly language program must have your name (computer) printed at the top. **ALL** simulations should be clearly annotated using Quartus annotations. Quartus files should be sent in a **Quartus archive file**. Grading emphasis will be placed upon your producing well documented, well structured design circuitry that realizes the functional requirements specified by the lab handout and the lab instructor. The remaining portion of your grade will result from observations by your lab instructor on such matters as your understanding of the lab, your lab techniques, your pre-lab preparation, your lab results and your cooperation and compliance with the rules. Having your design perform properly does **not** guarantee a grade of 100, but makes a 100 grade **possible**. Lab designs and/or software that are similar and/or identical to other student’s work constitute cheating (see above) and will be given a grade of 0 and reported to the professor for further discipline (and may result in failing the course, honor court charges, or expulsion). There will be a quiz at the beginning of most labs (worth up to 20% of your total lab score). If you are late for a lab, you will get a zero for the quiz.

RULES OF CONDUCT IN THE LABORATORY

1. No food, drinks or smoking in the lab.
2. Students work **individually** on each lab project.
3. It is the student's responsibility to return all equipment and clean her/his work area before leaving the lab.
4. Students must attend labs during their assigned time. If you cannot attend your normal lab, this lab will be your single (free) dropped lab.
5. Students must come prepared to the Lab. **No student will be admitted to the lab without the pre-lab work in hand,** *i.e.*, printout(s). Your files also must be submitted by email **BEFORE** the start of your lab.
6. You must show up within 30 minutes of the lab starting time for check-in or you will not be admitted. If you are more than 10 minutes late, you will get a zero for the lab quiz.
7. Quizzes might take as long as 1 hour (but could be shorter). Quizzes will be graded on a quaternary (also known as a quardary) scale of 0, 1, 2 or 3. This will translate into values of 0, 10%, 15%, or 20%, respectively to account for up to 20% of the lab grade. Quizzes will cover information from the pre-lab material and previous labs and course work.
8. Labs are precisely 3 hours long. You will be given no extra time.
9. The last 20 minutes of the lab is a time for student check-off and grading only, *i.e.*, no questions for the TA.

LABORATORY ATTENDANCE

Laboratory attendance during scheduled times is mandatory. **Documented** personal or family emergency will be accepted as an excuse for absence for a **second** missed lab if documentation for a first missed lab is also provided. In such cases, consult

SYLLABUS

Revision 5

your **instructor** (**not** your TA) about a make-up lab **as soon as possible**. See **Course Requirements** for more details. Students should make serious attempts on **all** labs. Grades less than 50% may be interpreted as not a serious attempt and may be scaled to 0.

You will **not** officially makeup your first missed lab. You should do this missed lab at home (or, if necessary, during a TA office hour) to be sure you understand the required material.

If you cannot finish the lab during the allotted time, you will lose at least 10% to 30% off your final score. You are expected to finish the labs on time. **The most successful students** generally get their labs to work **at home before their lab begins**.

LABORATORY TOPICS

Lab Number	Start Date	Topic
0	Thur, 25 Aug	Build your CPLD board
1	Tues, 6 Sept	Introduction to equipment, software and parts
2	Tues, 13 Sept	Logic design and implementation
3	Wed, 21 Sept	MSI circuit design and implementation
4	Thur, 6 Oct	Arithmetic Logic Unit (ALU) design and implementation
5	Mon, 17 Oct	Counter design and implementation
6	Mon 24 Oct	ALU / CPU design and implementation
7	Mon, 31 Oct	State Machine design and implementation
8	Tues, 15 Nov	CPU with ROM-based instructions
9	Mon, 28 Nov	G-CPU simulation and assembly language programming

EEL 3701 Schedule: Part 1 of 2

WEEK/DAY	DATE	LAB #	Lecture #	Tentative Weekly Topics / Comments	
1	M	22-Aug		Syllabus, web site	
1	Tu	23-Aug	1	Digital Design, Basic logic, Number Systems, Math	
1	W	24-Aug		Intro. to Quartus	
1	Th	25-Aug	0	2-3	Mixed Logic
1	F	26-Aug			
2	M	29-Aug	0		ICs, introduction to mixed, positive, and negative logic
2	Tu	30-Aug	0	4	Number Systems, Math
2	W	31-Aug	0		
2	Th	1-Sep		5-6	
2	F	2-Sep			
3	M	5-Sep		No class	Holiday: Labor Day
3	Tu	6-Sep	1	7	
3	W	7-Sep	1		Number Systems, Math
3	Th	8-Sep	1	8-9	Boolean Algebra
3	F	9-Sep			
4	M	12-Sep	1		MSOP, MPOS, Simplification
4	Tu	13-Sep	2	10	MSI: MUX, deMUX, decoder
4	W	14-Sep	2		K Maps
4	Th	15-Sep	2	11-12	
4	F	16-Sep			
5	M	19-Sep	2		More MSI: encoder, adder, BCD 7-seg decoder, tristate buffer
5	Tu	20-Sep		13	Arithmetic Logic Unit (ALU)
5	W	21-Sep	3		
5	Th	22-Sep	3	14-15	
5	F	23-Sep			
6	M	26-Sep	3		Introduction to sequential circuits: Flip-flops
6	Tu	27-Sep	3	16	Flip-flops and next state/excitation tables
6	W	28-Sep			Design with flip-flop, Counter design, Debouncing
6	Th	29-Sep		17-18	
6	F	30-Sep			

EEL 3701 Schedule: Part 2 of 2

WEEK/DAY	DATE	LAB #	Lecture #	Tentative Weekly Topics / Comments
7	M	3-Oct		EXAM 1, Mon: @ 6:15pm in _____
7	Tu	4-Oct	19	
7	W	5-Oct		
7	Th	6-Oct	4 20-21	
7	F	7-Oct		
8	M	10-Oct	4	Exam 1 Solutions / Regrade petitions submitted RAM, ROM PLDs, PALs Altera's MAX3000 and MAX7000 family CPLDs State machines: Mealy, Moore, ASM design
8	Tu	11-Oct	4 22	
8	W	12-Oct	4	
8	Th	13-Oct	23-24	
8	F	14-Oct		
9	M	17-Oct	5	ASM implementation, ASM design examples ASM design implementations, ROM based designs & others
9	Tu	18-Oct	5 25	
9	W	19-Oct	5	
9	Th	20-Oct	5 26-27	
9	F	21-Oct		
10	M	24-Oct	6	ASM design implementations, ROM based designs & others RAM/ROM expansion
10	Tu	25-Oct	6 28	
10	W	26-Oct	6	
10	Th	27-Oct	6 29-30	
10	F	28-Oct		
11	M	31-Oct	7	Introduction into computer architecture Homecoming
11	Tu	1-Nov	7 31	
11	W	2-Nov	7	
11	Th	3-Nov	7 32-33	
11	F	4-Nov	No class	
12	M	7-Nov		Introduction into computer architecture EXAM 2, Wed: @ 6:15pm in _____ Addressing modes, Data transfer instructions Holiday: Veteran's Day
12	Tu	8-Nov	34	
12	W	9-Nov		
12	Th	10-Nov	35-36	
12	F	11-Nov	No class	
13	M	14-Nov		Exam 2 Solutions / Regrade petitions submitted Basic computer operation cycles and timing Instruction set and assembly programming examples
13	Tu	15-Nov	8 37	
13	W	16-Nov	8	
13	Th	17-Nov	8 38-39	
13	F	18-Nov		
14	M	21-Nov	8	DROP DEADLINE (Monday, 5:00pm) Intro into computer architecture, registers, assembly & instructions Holiday: Thanksgiving Day Holiday: Thanksgiving Day Break
14	Tu	22-Nov	40	
14	W	23-Nov		
14	Th	24-Nov	No class	
14	F	25-Nov	No class	
15	M	28-Nov	9	G-CPU, Memory Maps G-CPU, Special topics
15	Tu	29-Nov	9 41	
15	W	30-Nov	9	
15	Th	1-Dec	9 42-43	
15	F	2-Dec		
16	M	5-Dec		G-CPU, Special topics and Review EXAM 3, Tues: @ 6:15pm in _____ (Classes End)
16	Tu	6-Dec	44	
16	W	7-Dec		