Department of Electrical & Computer Engineering Page 1/13

EEL 3701C: DIGITAL LOGIC AND COMPUTER SYSTEMS

https://mil.ufl.edu/3701/_

eel3701.slack.com

<u>UF's Canvas</u>

INSTRUCTOR Dr. Eric M. Schwartz ems@ufl.edu 352-392-2541 MALA 3110 Office Hours: Wed: 12:50pm, Fri 1:55pm

LECTURES

Tues 2nd-3rd (8:30-10:25am) & Thur, 3rd (9:35-10:25am) in MCCC 100 Tues 8th-9th (3:00-4:55pm) & Thur 9th (4:05-4:55pm) in PUGH 170

LABS (NEB 248) *PI = Peer Instructor (PI=UPI=Undergrad

5		Mon		Tues		Wed		Thur		Fri					
248)	Class #	Start	PI*	Class #	Start	PI	Class #	Start	PI	Class #	Start	PI	Class #	Start	PI
er Instructor =Undergrad PI)				11209-PM	9:35am		11195-PM	09:35am		11182-AM	10:40am		,, 11194-AM	09:35am	
o ,	25409-PM	11:45am		11181-AM	11:45am		11179-AM	11:45am		11211-PM	12:50pm		11178-AM	11:45am	
	16911-PM	1:55pm		11176-AM	1:55pm		11197-PM	1:55pm		11193-AM	4:05pm		11199-PM	1:55pm	
	20638-PM	4:05pm		11177-AM	4:05pm		11180-AM	4:05pm		11198-PM	6:15pm		11196-PM	4:05pm	
	11175-AM	6:15pm		11210-PM	6:15pm		21960-PM	6:15pm							

PREREQUISITES: None (but some knowledge of programming would be helpful)

REQIRED TEXTBOOK (Share, Borrow, Buy, or Rent one of the below. See https://mil.ufl.edu/3701/admin/3701_Textbook.pdf for more info)

- Charles H. Roth Jr., Fundamentals of Logic Design, Enhanced 7th edition, Cengage Learning, Stamford, Connecticut, 2021. ISBN: 1337620351
- Charles H. Roth Jr., Fundamentals of Logic Design, 7th edition, Cengage Learning, Stamford, Connecticut, 2014. ISBN: 1133628478
- Charles H. Roth Jr., Fundamentals of Logic Design, 6th edition, Cengage Learning, Stamford, Connecticut, 2009. ISBN: 0495471690
- Charles H. Roth Jr., Fundamentals of Logic Design, 5th edition, Thomson Brooks/Cole, Belmont, California, 2004. ISBN: 0534378048

RECOMMENDED REFERENCE TEXTBOOK

Reprinted Chapters 1-7 from H. Lam, and J. O'Malley, *Fundamentals of Computer Engineering: Logic Design and Microprocessors,* 1st edition, 1988, John Wiley and Sons, New York, available for **free** at <u>https://tinyurl.com/UF-Lam</u>.

COURSE OBJECTIVES (ABET Design Content 50%) [Lab fee: \$41.80]

<u>Official:</u> Overview of logic design, algorithms, computer organization and assembly language programming and computer engineering technology. Laboratory.

<u>Actual:</u> To learn to: perform elementary manipulations of Boolean algebraic equations; simplify logic expressions; design combinational and sequential circuits; use a digital design and simulation package, use a l description language (HDL), analyze binary storage device behavior and applications. Also to study the fundamentals of microprocessor architecture, including assembly language programming, and to understand the design of a basic microprocessor.

PI OFFICE HOURS

You may go to any PI available (in NEB 248, if no lab; else MAEB 328) for help during their office hours. (See the Faculty/PIs webpage for the office hours. Note that some office hours might be remote.) You are encouraged to use our slack's *#help* channel for questions. The slack rules for our course (as well as a link to join) are available <u>here</u>. E-mail (not a slack DM) is preferred for individual questions to communicate with the instructors and PIs. PIs will also hold a few help sessions, which will be announced in Slack.

Name	Nathan Achinger	Shreyas Adireddy	Landon Amaba	Alexander Aron	Benjamin Frohman	Enzo Gagliardo	Joseph Goodman	Grant Hillman	Keith Khadar	Kylie Lennon
e-mail	nathan.achinger@ufl.edu shreyas.adireddy@ufl.edu		amabal@ufl.edu	alexander.aron@ufl.edu	benjaminfrohman@ufl.edu	egagliardo@ufl.edu	jgoodman1@ufl.edu	hillman.grant@ufl.edu	kkhadar@ufl.edu	kylie.lennon@ufl.edu
Name	Sydni-Noel Levy-Davis	John Lewis	Jaiden Magnan	lan Santamauro	Alexander Schmidt	Benjamin Smith	Joseph Starr	Shan Sundal	Alexander Tishinin	Jack Wardlaw
e-mail	s.levydavis@ufl.edu	johnlewis@ufl.edu	jaiden.magnan@ufl.edu	isantamauro@ufl.edu	schmidtalexander@ufl.edu	smithbenjamin@ufl.edu	josephstarr@ufl.edu	shan.sundal@ufl.edu	atishinin@ufl.edu	jackwardlaw@ufl.edu

LECTURE FORMAT

The course lectures and labs will be entirely synchronous and in-person. This means that your lectures and labs will occur at the times specified when you registered. Nothing will be Zoomed this semester, other than our practicals and exams (and some office hours).

EXAM/PRACTICAL SCHEDULE

Each of our mid-term exams/practicals are administered in the evenings. All our Practicals and Exams will utilize Canvas and Honorlock with Zoom.

Exam/Practical Schedule

ITEM	Date	Time	Location
Practical 1	Mon, 3 Mar	8:20pm	Remote
Exam l	Thur, 6 Mar	8:20pm	Remote
Practical 2	Wed, 16 Apr	8:20pm	Remote
Final/Exam 2	Sat, 26 Apr	*3:00pm	Remote

*The likely time. Conflicts with ECO 2013: Principals of Macroeconomics

Department of Electrical & Computer Engineering Page 2/13

EEL 3701 — Spring 2025 SYLLABUS Revision Tentative

REQUIRED HARDWARE

- Below are supplied to students during their first lab, paid for with your lab fees. You probably <u>cannot</u> buy the kits separately, so please be careful as you design and construct your circuits this semester. See <u>Parts List</u> for more details.
 - *Digilent* Analog Discovery (DAD)
 - If you already have one (probably a DAD-2), you do not need to borrow one from ECE.
 - Students that borrow a DAD will get a DAD-3.
 - EE and CpE students will return during their last undergraduate semester in their BS degree program.
 - Other students will return before the end of the semester.
 - DE10-lite (*TerasIC*)
 - All students will return this (in its original packaging) at the end of the semester.
- You will need to **purchase** or somehow procure (i.e., these are not part of your lab fee) the following:
 - The following items (OOTB 3701 Parts Kit); OOTB = Out of the Box, a company founded by a UF student.
 - 40-pin IDC cable for DE10-lite
 - DE10-lite breadboard breakout
 - SPDT switch with breadboard breakout
 - 2 8 circuit SPST DIP switches for breadboard use
 - 2 10 circuit bar graph LEDs

2 - SIP resistor packs 2.2 kΩ × 8
2 - 74HC00 (NAND) ICs

• 2 - DIP resistor packs 1 k $\Omega \times 8$

- 2 74HC02 (NOR) ICs
- 2 Axial resistors 1 kΩ
- The below items are also required, but some of you may already have them. (You can get these as well as all the above items from OOT at <u>OOTB 3701 FULL Parts Kit</u> or from Jameco, as specified below.) These will be useful in other courses including EEL 3111C: *Circuits 1*.
 - Breadboard (with at least two panels) (Jameco Part #: 20774)
 - Wire kit (Jameco Part #: 19290)
- OOTB also sells an <u>IC Remover</u> that is very useful, but <u>not</u> required.
- The OOTB parts are **not available** for purchase until after January 1st, but there is an option on their website to be notified when they are available.

Your computer will need the necessary USB ports to connect to the DAD and DE10-lite. If it does not have the proper ports than you will need to purchase a USB Port Expander or USB Converter cable(s).

- The DAD-3 requires a female USB C type port on the PC (with a female USB C type port on the DAD-3).
- The DAD-2 requires a female USB A type port on the PC (with a female USB B type port on the DAD-2).
- The DE10-lite requires a female USB A type port on the PC (with a female USB B type port on the DE10).
- You <u>MUST</u> have and use your own laptop computer for this course with a dedicated camera and microphone. (A cell phone canNOT be used as the camera.)
- Because our quizzes, practicals, and final exam will all use Honorlock, you **must have a speaker** or set of speakers for your computer for these assignments. Neither headphones nor earbuds are allowed.

SOFTWARE REQUIREMENTS

Intel Quartus Prime (from Altera, now owned by Intel) is available to download, free of charge, from Altera's website and our website. With Quartus, you can design and simulate circuit design using either schematic entry or a hardware description language (e.g., VHDL and Verilog). Quartus will be used regularly, throughout the semester.

CLASS AND EXAM BEHAVIOR

Turn off all cell phones, laptop sound effects, and other noise making devices (including your microphone) before entering our classroom (or <u>Zoom</u>, for practicals and exams). If a noise-making device goes off during class, I reserve the right to lower your course grade. Do not visit websites, play games, or use other programs or apps during class that might distract nearby students.

GRADING POLICY

Grades are periodically posted on the class web site. It is your responsibility to check your grades regularly (on both Canvas and our course website) since mistakes often happen when dealing with a large number of students and PIs. All grades are final <u>one week</u> after posting (on either Canvas or our course website). After curving quizzes and our final exam as needed, course grades are assigned using the 60 (D), 70 (C), 80 (B), and 90 (A) cuts. $[90 \rightarrow 100 (A), 86.\overline{6} \rightarrow 89.\overline{9} (A-), 83.\overline{3} \rightarrow 86.\overline{6} (B+), 80 \rightarrow 83.\overline{3} (B), 76.\overline{6} \rightarrow 79.\overline{9} (B-), 73.\overline{3} \rightarrow 76.\overline{6} (C+), 70 \rightarrow 73.\overline{3} (C), 66.\overline{6} \rightarrow 69.\overline{9} (C-), 63.\overline{3} \rightarrow 66.\overline{6} (D+), 60 \rightarrow 63.\overline{3} (D), 56.\overline{6} \rightarrow 59.\overline{9} (D-), and 0<56.\overline{6} (E]].$

Part of your grade on exams, labs, homework, quizzes, etc. is based not only on solving a problem, but the manner in which you solve it. For example, there is a difference between two designs that meet the given specifications, but one is an elegant, modular 3-element solution, while the other is an obfuscated 7-element design that also meets the specifications but would be difficult to extend later. Just as your future employer would value the latter design less than the first, so will I in grading your assignments.

The UF grading policies for assigning grade points can be found on the following undergraduate catalog web page: https://catalog.ufl.edu/ugrad/current/regulations/info/grades.aspx.

All grades are **<u>non</u>**-negotiable <u>**one week**</u> after the grade is posted. Please don't come to me after the final grades have been posted with a hard-luck story.

Department of Electrical & Computer Engineering Page 3/13 EEL 3701 — Spring 2025 SYLLABUS Revision Tentative

COURSE GRADE DETERMINATION

Laboratory*	30%	(Lab values vary, i.e. it could count as 1/4 a lab, a single lab, a double lab, etc.)				
Homework	6%	(10-14)				
Exams 1	27%	_				
Practical 1	3%		All grades are <u>non</u> -negotiable <u>one week</u> after the grade is posted. Please don't come to me after the			
Practical 2	7%		inal grades have been posted with a hard-luck stor			
Final/Exam 2	27%					
Total * *	100%	(90+ on combined Final/Exam 2 and Practical 2 results in 5% gra	ade bonus, e.g., $86\% \Rightarrow 91\%$)			

Perform all laboratory experiments. A grade of 65% or better for your lab weighted average is <u>required</u> in order to be eligible to obtain a passing grade in the course (i.e., to earn a grade better than E). Your lowest lab (escluding Lab 6) will be dropped if you submit the pre-lab late, as specified in § Laboratory Attendance. But use this drop wiely, i.e., do <u>nof</u> just skip a lab since all labs are important and your next missed lab may be unavoidable. If you neve a valid reason for missing this lab, get documentation for your <u>first</u> missed lab and <u>hold on to it</u>. If you must show Dr. Schwartzhont a P] written documentation for your <u>first</u> missed lab and hold on to it. If you must show Dr. Schwartzhont a P] written documentation for missed lab of sevent as second lab, you must show Dr. Schwartzhont a P] written documentation for sevent missed lab or a soon as possible after your second missed lab. There is <u>prior</u> to your exam (or other event) so that an alternate lab time might be arranged.

Note: All grading percentages are subject to change at Dr. Schwartz's discretion. Students will be notified of any changes.

I have found that attendance directly correlates to grades. Therefore, attendance is required but is **NOT** worth positive points. Each missed class (or a poor attendance quiz result) results in a deduction of one point (out of 100) from your overall course total. There are no excuses for missed classes, but three classes can be missed without penalty. (Since there are 27 classes this semester, missing four classes is 15%! We will have attendance quizzes through Canvas, each with a simple general question (worth 30%), at least for those that have attended and paid attention to our lectures and submitted previously due assignments. There will also be an "are you there" question worth 70%. You can miss $13 (= 13 \times 0.3 = 3.9 < 4)$ of the simple general attendance quiz questions with absolutely no penalty if you attend each class **and** answer the "are you there" question positively. There are no excuses for missed classes. Late arrival or early departure (when missing the quiz) will count as an absence. To take the attendance quizzes, you will either need your laptop or a smart phone, running Canvas. Attendance quizzes can happen at any time from the beginning to the end of a lecture, so come to class on time. Leaving class after an attendance quiz but before the end of class is considered cheating unless advanced is provided (with a reason given) to Dr. Schwartz and permission is granted.

EXTRA CREDIT

Extra credit is sometimes offered during class (or on the web, by slack, or by email). The amount of extra credit given is at the discretion of Dr. Schwartz unless specifically stated with the extra credit opportunity.

SCANNING SOFTWARE

Some parts of homework, labs, and exams may require you to scan some of your handwritten work. Both *CamScanner* (<u>http://www.camscanner.com/user/download</u>) and *Fast Scanner* (<u>http://www.coolmobilesolution.com/</u>) are available for Android Phone and iPhone. Install one and email a scan to yourself to verify that it works. Learn how to get clear scans; black and white scans are usually best and are required for exams. Unclear scans **will not** be accepted. Unless other specified, when scans are requested for a particular assignment, a **single pdf** document should be created and submitted. You also are expected to have access to a cell phone camera for taking pictures of various hardware designs that you may construct during the semester for homework, labs, and exams.

HOMEWORK GRADING

Homework is submitted through Canvas by the assigned deadline. Unless other specified (sometimes additional files are requested), a **single pdf** document should be submitted for each homework. You must use the homework template provided on our website. Scans of various parts of the homework are acceptable, but the scans must be incorporated into the single submission document. Missed homework can<u>not</u> be made up, but your lowest homework is dropped. Homework solutions are sometimes posted on our class website **before** they are due. It is <u>not</u> appropriate to copy the supplied solutions; this constitutes cheating. Homework will only be graded in a cursory fashion, i.e., Zen grading is used. The grades will be entered into the grade book as 0 (no significant effort or not submitted), 1 (half-hearted attempt), or 2 (significant attempt). The final course grades will be assigned with strict cuts between grades, but HW <u>could</u> push you above a cut. In addition, the exams will be partly based on the assigned homework. Since graded homework is not returned and is graded only for effort, students should compare their solutions to the posted solutions. <u>Late homework is not accepted</u>.

Department of Electrical & Computer Engineering Page 4/13

EXAM RE-GRADE POLICY

If you believe an error has been made on an exam score you must make an <u>email</u> request (with subject 3701: Exam X Regrade Petition) to Dr. Schwartz explaining where the misgrading or error occurred. This request must be submitted <u>immediately at the end of the</u> class in which the exam is returned. If you do resubmit an exam for regrade, the instructor reserves the right to scrutinize and regrade the <u>entire</u> exam. This definitely places your current score at risk. Consequently, it is not advisable to resubmit an exam for correction unless a blatant error, such as a miscalculation of total points, has been made. You <u>must</u> make it clear what writing you added to the exam (by clear indication, e.g., use a different color pen or pencil) after it was graded.

HOMEWORK SOLUTIONS, PRACTICE EXAMS, AND COURSE GRADES

Solutions to homework will be made available on our class web site at <u>https://mil.ufl.edu/3701/</u>, along with periodic postings of your grades and the class grade statistics. Practice exams (a few with solutions and many without solutions) are also posted.

COURSE REQUIREMENTS (IMPORTANT !!!)

- 1. Perform all laboratory experiments. A grade of 65% or better is your lab weighted average is <u>required</u> in order to be eligible to obtain a passing grade in the course (i.e., to earn a grade better than E). Your lowest lab (not including Lab 6) will be dropped if you submit the pre-lab late, as specified in § Laboratory Attendance. But use this drop wisely, i.e., do <u>not</u> just skip a lab since all labs are important and your next missed lab may be unavoidable. If you need to miss a single lab, it's ok; you <u>cannot</u> make up the missed lab. (You should do this lab on your own. If necessary, you may visit a PI during office hours for help.) If you have a valid reason for missing this lab, get documentation for your <u>first</u> missed lab and hold on to it. If you miss a <u>second</u> lab, you must show <u>Dr. Schwartz</u> (not a PI) written documentation for BOTH your first and your second missed labs. This documentation should be official and from a doctor, judge, etc., so that a make-up can be arranged. You must notify Dr. Schwartz at least 8 days prior to your second missed lab. There is <u>rarely</u> an excuse that will allow you to reschedule your first missed lab other than an <u>exam</u> in another course or an officially sanctioned academic event. You must notify Dr. Schwartz at least 8 days prior to your exam (or other event) so that an alternate lab time might be arranged.
 - If you believe that you have valid university-related reason for missing a particular lab (e.g., Lab X), send an **email** to Dr. Schwartz with the following information (with subject: *3701: Conflict with Lab X*, where X is the lab number).
 - State the cause for missing your Lab X and provide associated documentation for this event.
 - Info about your normally scheduled Lab X. i.e., PI's name, Lab X date (day and date) and time, lab class number (5 digits)
 - o Lab X dates (class number, day, and date) that you will be **unavailable** for your Lab X.
 - ALL other scheduled Lab X dates (day, date, periods, times, class number) that you will be **available** for your Lab X (in order of your preference). Note that I will try to accommodate your preference <u>AFTER</u> I try to find a lab with available space.
 - If this is for an exam in another course, <u>first</u> verify that there are no alternate exam times available. If none, then provide Dr. Schwartz the course number and course name, and your teacher's name, email, and phone number. Also provide a link or screen shot of the cause of the conflict.
 - Labs <u>must</u> be done at scheduled times (except as described above).
 - Students <u>must</u> be prepared to demo their lab when they enter. Students will be randomly selected for their demonstration times during their lab period.
 - An average lab grade of <u>65% or higher</u> is required to be <u>eligible</u> to <u>pass</u> the course!
- 2. Class attendance is mandatory. The roll will be taken by means of a short Canvas quiz. (The quiz is normally very simple material from the prior class or presented previously in the class in which the quiz is administered.) Each missed class when roll is taken (or a poor attendance quiz result) will cost one point (out of 100) from your overall course total. The roll may be taken more than once in class; if you leave and a second roll is taken, this will be interpreted as an honor code violation.
 - No excuses accepted, but FOUR free drops.
 - Missed classes <u>cannot</u> be made up.
 - If you miss the first two classes and do not notify me, you will be dropped from the course.
- 3. Complete and submit all homework assignments and turn them in <u>through Canvas before</u> the time that they are due.
- Late homework will <u>not</u> be accepted.
- 4. Lab submissions are due at least fifteen minutes before your lab. See the <u>Lab Rules & Policies</u> for more details.
- 5. Take all quizzes, exams, and practicals as scheduled.
 - No makeup quizzes, exams, or practicals will be given except in cases of a medically documented incapacity or family emergency.
 - If you believe that you have a valid exam or practical conflict, please send me the info specified above for a lab conflict (again, at least **8 days** in advance), but with the subject: *3701: Conflict with TYPE X*, where TYPE is the type of assignment (Quiz X, Exam, Practical X) and X is the assignment number. Specify the times of your conflict, the cause of your conflict, and then times immediately before or after the scheduled exam time when you **are** available.
 - If the conflict is an exam in another course, verify that there are no alternate exam times available. If none, then provide Dr. Schwartz with the course number and name, and your teacher's name, email, and phone number. Also provide a link or screen shot of the cause of the conflict.

Department of Electrical & Computer Engineering Page 5/13

STUDENTS REQUIRING ACCOMMODATIONS

The University of Florida is committed to providing academic accommodation for students with disabilities. Students with disabilities requesting accommodations should first register with the Disability Resource Center (352-392-8565, <u>https://disability.ufl.edu/</u>) by providing appropriate documentation. See <u>https://disability.ufl.edu/students/get-started/</u> to start the process to request academic accommodations. Once registered, a student should present his/her accommodation letter to me supporting a request for accommodations. The University encourages students with disabilities to follow these procedures as early as possible within the semester. For your optimal benefit, you must see the professor **during the first week of classes**.

COMMITMENT TO A SAFE AND INCLUSIVE LEARNING ENVIRONMENT

The Herbert Wertheim College of Engineering values broad diversity within our community and is committed to individual and group empowerment, inclusion, and the elimination of discrimination. It is expected that every person in this class will treat one another with dignity and respect regardless of gender, sexuality, disability, age, socioeconomic status, ethnicity, race, and culture. If you feel like your performance in class is being impacted by discrimination or harassment of any kind, please contact your instructor or any of the following:

- Your academic advisor or Graduate Program Coordinator
- Jennifer Nappo, Director of Human Resources, 352-392-0904, jpennacc@ufl.edu
- Curtis Taylor, Associate Dean of Student Affairs, 352-392-2177, taylor@eng.ufl.edu
- Toshikazu Nishida, Associate Dean of Academic Affairs, 352-392-0943, nishida@eng.ufl.edu

HEALTH AND WELLNESS (UF COUNSELING SERVICES)

Resources are available on-campus for students having personal problems or lacking clear career and academic goals. The resources include:

- University Counseling & Wellness Center, <u>https://counseling.ufl.edu/</u>, 3190 Radio Road, (352) 392-1575.
- SHCC mental Health, Student Health Care Center, http://shcc.ufl.edu/, Infirmary Building, 1 Fletcher Drive, 392-1161.
- U Matter, We Care, <u>http://www.umatter.ufl.edu/</u>, umbrella organization for UF's caring culture and provides students in distress with support.

<u>U Matter, We Care</u>

- Your well-being is important to the University of Florida. The *U Matter, We Care* initiative is committed to creating a culture of care on our campus by encouraging members of our community to look out for one another and to reach out for help if a member of our community is in need.
- If you or a friend is in distress, please contact <u>umatter@ufl.edu</u> so that the *U*^o*Matter*, *We Care* Team can reach out to the student in distress. A nighttime and weekend crisis counselor is available by phone at 352-392-1575. The *U Matter, We Care* Team can help connect students to the many other helping resources available including, but not limited to, Victim Advocates, Housing staff, and the Counseling and Wellness Center.
- Please remember that asking for help is a sign of strength. In case of emergency, call 9-1-1.
- Counseling and Wellness Center: <u>http://www.counseling.ufl.edu/cwc</u> and 392-1575
- University Police Department: 392-1111 or 9-1-1 for emergencies.
- Sexual Discrimination, Harassment, Assault, or Violence: If you or a friend has been subjected to sexual discrimination, sexual harassment, sexual assault, or violence contact the <u>Office of Title IX Compliance</u>, located at Yon Hall Room 427, 1908 Stadium Road, (352) 273-1094, <u>title-ix@ufl.edu</u>
- Sexual Assault Recover Services (SARS): Student Health Care Center, 392-1161
 - Resources for Sexual Violence, <u>https://umatter.ufl.edu/helping-students/sexual-violence-response/</u>, Immediate Response/Advocacy 392-5648 or 392-1111; Medical Care from Student Health Care Center, 392-1161
 - University Police Department: 392-1111 or <u>http://www.police.ufl.edu</u> 9-1-1 for emergencies.

ACADEMIC RESOURCES

- E-learning technical support, <u>https://elearning.ufl.edu/</u>, 392-4357, email: <u>Learning-support@ufl.edu</u>...
- Career Connections Center, <u>https://career.ufl.edu/</u>, 392-1601. Reitz Union. Career development assistance and counseling.
- Library Support, <u>https://uflib.ufl.edu/find/ask/</u>.
- Teaching Center, <u>https://teachingcenter.ufl.edu/</u>, 392-2010. Broward Hall. General study skills and tutoring.
- Writing Studio, <u>https://writing.ufl.edu/writing-studio/</u>, 846-1138, 302 Tigert Hall. Help brainstorming, formatting, and writing papers.
- Ombuds office, <u>https://ombuds.ufl.edu/</u>. Ombuds office exists to assist students, staff, and faculty in resolving problems and conflicts.
- Student Complaints, Campus: <u>https://sccr.dso.ufl.edu/policies/student-honor-code-student-conduct-code/</u>, <u>https://care.dso.ufl.edu</u>.
- On-Line Students Complaints, http://www.distance.ufl.edu/student-complaint-process

Department of Electrical & Computer Engineering Page 6/13

COURSE EVALUATION

Students are expected to provide professional and respectful feedback on the quality of instruction in this course by completing course evaluations online via GatorEvals. Guidance on how to give feedback in a professional and respectful manner is available at https://gatorevals.aa.ufl.edu/students/. Students will be notified when the evaluation period opens and can complete evaluations through the email they receive from GatorEvals, in their Canvas course menu under GatorEvals, or via https://ufl.bluera.com/ufl/. Summaries of course evaluation results are available to students at https://gatorevals.aa.ufl.edu/public-results/.

LECTURES, LABS, QUIZES, PRACTICALS, AND EXAMS (CANVAS, ZOOM, AND HONORLOCK)

During this semester, the course will be entirely synchronous. This means that your lectures and labs will occur at the times specified when you registered. Practicals and the midterm exam will be administered in the evening.

Lectures are face-to-face. Your course assignments will be specified on Canvas, but more information will be available on our website (<u>www.mil.ufl.edu/3701/</u>). All homework and lab submissions will be through Canvas. Quizzes, practicals, and exams will be administered through Honorlock (and Canvas Quizzes) with Zoom. Honorlock is an online proctoring service. Sometime during most classes, there will be a Canvas Quiz; therefore, all students will need computers (or at least a smart phone).

Open Canvas at the beginning of every class; keep it open during class for the inevitable attendance quiz.

For our practicals and exams, utilized so that you can ask questions, you will need to log into <u>Zoom</u> (at <u>https://ufl.zoom.us/</u>) before being admitted to the Zoom. You must connect to Zoom **prior** to connecting to the Honorlock quiz. Use your full name for our Zoom meetings, i.e., no nicknames or other alternatives.

Students can take practicals and exams in the course from almost any location, as long as you have a computer, and the following required components: webcam, microphone, speaker, and a stable Internet connection. Minimum upload and download bandwidths of 2 Mbps are required for this course. If you don't have the bandwidth or any of this hardware, you must secure these in order to participate in the course. (Note that **neither** headphones **nor** earbuds are allowed during Honorlock administered assignments.)

For Honorlock, you do not need to create an account but will need Google Chrome (available from <u>www.google.com/chrome/</u>). You will also need to download the Honorlock Chrome Extension (from <u>www.honorlock.com/extension/install</u>).

Prior to any Honorlock quiz, the following has been shown to prevent problems that have happened to a few students in the past. I recommend that you do this prior to **EVERY** Honorlock quiz.

- Close all programs/applications other than Chrome.
- Close all Chrome tabs other than Canvas.
- Turn off any potential pop-ups that could occur during this assignment.

Below has been shown to prevent problems that have happened to a few students in the past. It wouldn't hurt and could save some aggravation.

- Exams and practicals will use Zoom. Verify that you have the Zoom link.
- Verify that your cell phone has scanning software (e.g., *CamScanner*).
- Verify that you have your UF ID, passport, or driver's license (for the Honorlock ID scan).
- Clear your cache in Chrome.
 - Reinstall the Chrome Honorlock Extension.
 - Close then reopen Chrome.
- Verify that you have the files that you need (and are allowed, if any) available from your desktop.
- Reboot your computer.
- Be sure that there is enough light for your face to be clear during the exam (for Honorlock). If not, find a lamp or a different area.

When you are ready to start an Honorlock-monitored assignment (quiz, practical, or exam), connect to the relevant Zoom assignment and then select "Stop Video" in Zoom (to stop Zoom from using your webcam). Now log into Canvas, go to our course, and click on the appropriate assignment. Click "Launch Proctoring" to begin the Honorlock authentication process, where you will take a picture of yourself, show your ID, and show both sides your blank scratch paper (if allowed). If requested by Honorlock staff or course staff, perform a complete scan of your room; an adequate room scan should take approximately one minute. Honorlock will record your exam session by webcam and also record your screen. Honorlock also has an integrity algorithm that can detect search-engine use, so please do not attempt to search for answers, even if it's on a secondary device. There are many more Honorlock rules; these should be investigated **BEFORE** your relevant Honorlock-monitored assignments.

As part of your Honorlock-monitored assignment, at the end of the timed session, you may be asked to scan some of your exam work. See § *SCANNING SOFTWARE* for more information.

Department of Electrical & Computer Engineering Page 7/13

For emergency situations, learn how to turn your cell phone into a hotspot to use if your internet goes out. If you ever have WiFi or other internet connection problems just before or when your practical, quiz, or exam, use your phone to send <u>BOTH</u> a slack message <u>and</u> an email to the relevant PI <u>and</u> Dr. Schwartz. Describe the issue and continue to **try to get back in**! Use your cell phone as an emergency hotspot.

Honorlock support is available 24/7/365. If you encounter any issues during a quiz, exam or other assignment, **contact Honorlock by live chat**, which should be available inside Honorlock. If you are kicked out of Honorlock, you can go to <u>https://honorlock.com/support/</u> and select *begin live chat*. At other times, i.e., not during a monitored assignment, you can send Honorlock an email (<u>support@honorlock.com</u>). Zoom chat Dr. Schwartz or other course staff if this problem occurs during an Honorlock-monitored assignment. If your internet goes out, connect to Zoom with your cell phone.

PRACTICALS

Practicals are exams where you design, simulate, build, and demonstrate in a fixed amount of time. You have no access to any previous assignments or solutions but may have access to necessary documentation. You must have a single solution, i.e., you cannot modify your solution during the demonstration to show parts of the solution and then modify or use another program to demonstrate a second part of the solution. You only earn points for parts that are demonstrated to function properly. No points are awarded for designs that are not demonstrated to work. If you cannot solve the entire problem, it is up to you to determine a procedure that can demonstrate what does work (but still are allowed only a single solution).

IN CLASS RECORDING

Students are allowed to record video or audio of class lectures. However, the purposes for which these recordings may be used are strictly controlled. The only allowable purposes are (1) for personal educational use, (2) in connection with a complaint to the university, or (3) as evidence in, or in preparation for, a criminal or civil proceeding. All other purposes are prohibited. Specifically, students may not publish recorded lectures without the written consent of the instructor.

A "class lecture" is an educational presentation intended to inform or teach enrolled students about a particular subject, including any instructor-led discussions that form part of the presentation, and delivered by any instructor hired or appointed by the University, or by a guest instructor, as part of a University of Florida course. A class lecture does **not include lab sessions**, student presentations, clinical presentations such as patient history, academic exercises involving solely student participation, assessments (**quizzes, tests, exams**), field trips, private conversations between students in the class or between a student and the faculty or lecturer during a class session.

Publication without the permission of the instructor is prohibited. To "publish" means to share, transmit, circulate, distribute, or provide access to a recording, regardless of format or medium, to another person (or persons), including but not limited to another student within the same class section. Additionally, a recording, or transcript of a recording, is considered published if it is posted on or uploaded to, in whole or in part, any media platform, including but not limited to social media, book, magazine, newspaper, leaflet, or third-party note/tutoring services. A student who publishes a recording without written consent may be subject to a civil cause of action instituted by a person injured by the publication and/or discipline under UF Regulation 4.040 Student Honor Code and Student Conduct Code.

SOFTWARE USE

All faculty, staff, and students at the University are required and expected to obey the laws and legal agreements governing software use. Failure to do so can lead to monetary damages and/or criminal penalties for the individual violator. Because such violations are also against university policies and rules, disciplinary action will be taken as appropriate. We, the members of the University of Florida community, pledge to uphold ourselves and our peers to the highest standards of honesty and integrity.

TECHNOLOGY

The use of cell phones and **every other** technology device not directly specified as allowed is strictly prohibited during live assignments (labs, quizzes, practicals, or exams). All use of disallowed electronic devices (or inappropriate use of allowed devices) during a live assignment will be considered a violation of the student honor code (i.e., cheating). See the *Honesty Policy* section below for the minimum penalties that are incurred for all cases of cheating in our course. Laptop computers and tablets are welcome in class as long as they are used for class-related work. Surfing the web, checking email, making posts, etc., is strictly prohibited (**if distracting to others**) and will result in course grade deductions.

During practical exams, no use of the internet (other than Zoom and Honorlock) or other external access is **not** allowed. You may only use files and software on your computer that are explicitly specified by Dr. Schwartz several days prior to the practical exam date.

TWO-STEP AUTHENTICATION BACKUP

You should get a backup for the two-step authentication. See <u>https://it.ufl.edu/2fa/using2fa/</u>. There have been students who had cell phone issues (like a lost phone, a broken phone, or the battery runs out) just before starting a quiz/exam and were unable to participate. To assure that you can always get in, generate some passcodes and always keep them with you (perhaps in your wallet). To do this, go to <u>account.it.ufl.edu</u> and select *GENERATE PASS CODES*; then login and authenticate as normal and select the *GENERATE PASS CODES* button. Five codes will appear, each of which will work once and do not expire.

Department of Electrical & Computer Engineering Page 8/13

EEL 3701 — Spring 2025 SYLLABUS Revision Tentative

STUDENT PRIVACY

There are federal laws protecting your privacy with regards to grades earned in courses and on individual assignments. For more information, please see <u>https://registrar.ufl.edu/ferpa.html</u>.

COMMUNICATION

Slack is utilized for course announcements. You are also responsible for getting the slack. You are also responsible for regularly checking announcements and course-related postings on the class website, Canvas, and your UF email.

MULTIMEDIA CLASS/AUDIENCE NOTES

Audience notes are normally posted on the class web site every week or so for the subsequent week or more of classes. The notes consist of pdf versions of the class PowerPoint slides. These notes are not required but are <u>highly</u> recommended. Check the class web site for information on exactly when the notes are available. <u>For optimal performance</u>, read the notes and examples for a class <u>before</u> that class and augment the material with your own notes during class. My notes are removed shortly after they are covered in class.

Historically, student that take good notes perform much better in this course then those who do not take notes (or take poor notes). Augmenting my notes with your own is strongly encouraged.

All grades are <u>non</u>-negotiable <u>one</u> <u>week</u> after the grade is posted. Please don't come to me after the final grades have been posted with a hard-luck story.

HONESTY POLICY

UF students are bound by The Honor Pledge which states, "We, the members of the University of

Florida community, pledge to hold ourselves and our peers to the highest standards of honor and integrity by abiding by the Honor Code. On all work submitted for credit by students at the University of Florida, the following pledge is either required or implied: "On my honor, I have neither given nor received unauthorized aid in doing this assignment." The Honor Code (https://sccr.dso.ufl.edu/process/student-conduct-code/) specifies a number of behaviors that are in violation of this code and the possible sanctions. Furthermore, you are obligated to report any condition that facilitates academic misconduct to appropriate personnel. If you have any questions or concerns, please consult with the instructor in this class.

Artificial intelligence (AI) language models, such as ChatGPT, and online assignment help tools, such as Chegg®, are examples of online learning support platforms: they can<u>not</u> be used for course assignments except as explicitly authorized by the instructor. The following actions are prohibited in this course:

- submitting all or any part of an assignment statement to an online learning support platform
- incorporating any part of an AI generated response in an assignment
- using AI to summarize or contextualize source materials
- submitting your own work for this class to an online learning support platform for iteration or improvement

Each quiz, practical, and exam should be assumed to contain that specific pledge; opening and starting the assignment commits you to honoring that pledge.

CHEATING WILL NOT BE TOLERATED. We will actively search for cheaters; we have and will use excellent software to help us in the search. If you are caught, there will be no negotiations. You will earn a course grade penalty (almost always failure for the course) and get reported to UF's honor court. There are **no excuses and no exceptions**. You may talk to other students about assignments, but the final work **must** be your own. You must also report others (anonymously, if desired) that you suspect are cheating. If you are caught cheating on **any** assignment (homework, lab, or exam, etc.), you **will** be prosecuted. A meeting with the UF honor court, along with the instructor, will determine penalties, none of which are desirable or pleasant (*i.e.*, cheating in this course always results in notification to the honor court, often results in a failing grade in the course, and can possibly result in suspension or expulsion from the university). If you know someone is cheating, **it is your responsibility to report it**. For more information about cheating, the UF Honor code, and the consequences of academic dishonesty, please refer to https://sccr.dso.ufl.edu/policies/student-honor-code-student-conduct-code/. If you have any questions or concerns, please consult with Dr. Schwartz. The flow chart for an honor code violation is available <u>here</u>. A link to report an academic honesty incident is available <u>here</u>.

Each lab section has their own lab quiz. Because there are labs on every day of the week, some students will have their lab up to six days before other students. Since all quizzes for a particular lab will each be coving the same topics, information about another section's quiz would give a student an unfair advantage over other students. Therefore, while the lab is still ongoing, you should \underline{NOT} 1) communicate with other students in the course (outside of your own lab section) about your lab quiz, 2) give hints about the quiz, or 3) ask any other student for information about their quiz. Sharing lab quiz information in any of these ways is a violation of the honor-code policy and will be treated as such.

Department of Electrical & Computer Engineering Page 9/13 EEL 3701 — Spring 2025 SYLLABUS Revision Tentative

WORKING TOGETHER

You are encouraged to work with other students on assignments in a professional manner. Each person in the group should attempt to solve all problems **independently** and **only** then discuss the results with one's partner(s) to correct errors. Copying your partner's work constitutes cheating and should not be permitted. All solutions should reflect your style of problem solving. You may **not** copy and submit old or new posted solutions as if they were your own.

Although you may **consult** with other students, PI's, or **instructors** for your assignments, you **must** do independent work. Consulting means **"seeking opinions or advice," not** getting working solutions, programs, or designs, understanding them, and then modifying them to make them your own. The latter constitutes cheating (see above section). Working side-by-side to find a solutions, construct a program, or design in a group constitutes cheating. (Solving homework are good practice for solving quizzes and exams, which are also **not** group activities.) **You should note that we have used and will continue to use software that can detect similar submissions.**

Failure to do your own work in lab will likely result in failure in these associated quizzes, practicals, and exams.

HANDOUTS

Handouts are supplied on-line and can be downloaded from the class web site: https://mil.ufl.edu/3701/.

LABORATORY RULES & POLICIES

See <u>Lab Rules & Policies</u> for important information that you should re-read prior to each lab submission. Prior to the start of your first lab, you must sign and submit this document (as proof that you understand and will follow the rules) or you will not be admitted to the lab.

LABORATORY GRADING

You will not be admitted to the lab without a previously submitted *Pre-Lab Report*, as described in the *Lab Rules & Policies*. The *Pre-Lab Report* and other files also must be submitted through Canvas at least fifteen minutes **BEFORE** the start of your lab.

Each circuit diagram, VHDL file, and assembly language program must have your name (computer) printed at the top. <u>ALL</u> simulations should be clearly annotated. Quartus files should be sent in a **Quartus** <u>archive</u> file. Demonstration videos of up to three minutes must also be submitted prior to your lab.

Grading emphasis will be placed upon your producing well documented, well-structured design circuitry that realizes the functional requirements specified by the lab handout and the lab instructor. The remaining portion of your grade will result from observations by your lab instructor on such matters as your understanding of the lab, your lab techniques, your pre-lab preparation, your lab results and your cooperation and compliance with the rules. Having your design perform properly does <u>not</u> guarantee a grade of 100, but makes a 100 grade <u>possible</u>. Lab designs and/or software that are similar and/or identical to other student's work constitute cheating (see above) and will be reported to the professor for further discipline (and will result in failing the course, honor court charges, or expulsion). There will be a quiz at the beginning of most labs (worth up to 40% of your total lab score). If you are late for a lab, you will get a zero for the quiz.

LABORATORY ATTENDANCE

Laboratory attendance during scheduled times is mandatory. **Documented** personal or family emergency will be accepted as an excuse for absence for a <u>second</u> missed lab if documentation for a <u>first</u> missed lab is <u>also</u> provided. In such cases, consult Dr. Schwartz (<u>not</u> your PI) about a make-up lab as soon as possible. See *Course Requirements* for more details. Students should make serious attempts on <u>all</u> labs. Grades less than 50% may be interpreted as not a serious attempt and may be scaled to 0. Note: <u>ALL</u> students <u>MUST</u> have everything working <u>BEFORE</u> coming to lab.

Failure to attend your scheduled lab will result in a lab grade of zero, even if you previously submitted the required lab documents.

You will <u>not</u> officially makeup your dropped lab. But you must submit the missed (and complete) pre-lab report within one week of when the pre-lab report was originally due. For the last lab, the submission deadline is 24 hours after your Lab 7 was due. You will lose the lab drop if you fail to submit a complete pre-lab document by the specified deadline. You should do this missed lab at home (or, if necessary, during a PI office hour) to be sure you understand the required material.

Lab Number	Probable Topic s						
0	Build your PLD board; intro to software and parts						
1	Quartus intro; Logic design and implementation (with discrete parts)						
2	MSI circuit design and implementation (with discrete parts & PLD)						
3	Counter design and implementation						
4	Registered Arithmetic Logic Unit (RALU) design & implementation						
5	State Machine design and implementation						
6	CPU with ROM-based instructions						
7	G-CPU simulation and assembly language programming						

LABORATORY TOPICS

Department of Electrical & Computer Engineering Page 10/13

EEL 3701 — Spring 2025 SYLLABUS Revision Tentative

RECIPE FOR SUCCESS

Recently, a student asked me how to be successful in our course. I thought it would be helpful to share what I told the student.

- 0) Take care of your health by sleeping well, eating well, and participating in some regular physical activity.
- 1) Attend all classes and actively participate by trying to follow the material, take notes, and ask questions (or write them down to ask later), if you have any.
- 2) Work a little bit on the material every day, even if it is only for an hour.
- 3) Review the notes within 24 hours after a class and read the relevant textbook sections (if any) with 24-48 from that class.
- 4) Complete all homework.
- 5) Read a lab as soon as we have gone over the relevant material. Start on it ASAP, at least trying to understand it.
- 6) Start each lab a week in advance of the due date, generally immediately after your prior lab is done.
- 7) When you don't understand something, immediately ask a question in class, slack, or go to the next PI, or faculty office hour.
- 8) If there are help sessions, participate!
- 9) Before exams, complete several practice exams. Do these **BEFORE** the exam help session(s) and then go to the help sessions to see the PIs solve those exams.

Department of Electrical & Computer Engineering Page 11/13

EEL 3701 — Spring 2025 SYLLABUS Revision Tentative

EEL 3701 Schedule: Part 1 of 2

WE	EK/DAY	DATE	LAB	LECTU RE	Tentative Weekly Topics / Comments
1	М	13-Jan	No lab	No class	UF classes begin (but not ours!)
1	Tu	14-Jan		1-2	Syllabus, web site; Digital Design, Basic logic
1	W	15-Jan			Intro. to Quartus
1	Th	16-Jan		3	
1	F	17-Jan			Drop/Add ends Friday at 11:59pm
2	М	20-Jan	No lab	No class	Holiday: Martin Luther King Jr. Day
2	Tu	21-Jan		4-5	Truth (Logic) Table / Voltage Table
2	W	22-Jan			Mixed Logic
2	Th	23-Jan		6	ICs, introduction to mixed, positive, and negative logic
2	F	24-Jan			
3	М	27-Jan	0		Number Systems, Math CCC AI & CISE: Jan 27, 1-6pm
3	Tu	28-Jan	0	7-8	CCC Non-Tech Day: O'Connell Center, Jan 28 9am-3pm Boolean Algebra
3	W	29-Jan	0		CCC Tech Day: O'Connell Center, Jan 29 9am-3pm MSOP, MPOS, Simplification
3	Th	30-Jan	0	9	
3	F	31-Jan	0		
4	М	3-Feb	1		MSI: MUX, deMUX, decoder
4	Tu	4-Feb	1	10-11	
4	W	5-Feb	1		
4	Th	6-Feb	1	12	
4	F	7-Feb	1		
5	М	10-Feb			More MSI: encoder, adder, BCD 7-seg decoder
5	Tu	11-Feb		13-14	Even more MSI: tristate buffer, ALU
5	W	12-Feb			Introduction to sequential circuits: Flip-flops
5	Th	13-Feb	2	15	
5	F	14-Feb	2		
6	М	17-Feb	2		Flip-flops and next state/excitation tables
6	Tu	18-Feb	2	16-17	Design with flip-flop, Counter design, Debouncing
6	W	19-Feb	2		19 Feb: Engineer's Fair, Reitz Grand Ballroom, 3-7pm
6	Th	20-Feb		18	
6	F	21-Feb			
7	М	24-Feb	3		MSI sequential circuits - Registers, counters
7	Tu	25-Feb	3	19-20	RAM/ROM
7	W	26-Feb	3		
7	Th	27-Feb	3	21	
7	F	28-Feb	3		

Department of Electrical & Computer Engineering Page 12/13

EEL 3701 Schedule: Part 2 of 2

WE	EK/DAY	DATE	LAB	LECTURE	Tentative Weekly Topics / Comments
8	М	3-Mar			Practical 1: Mon, 3 Mar, 8:20pm
8	Tu	4-Mar		22-23	State Machines
8	W	5-Mar			
8	Th	6-Mar		24	Exam 1: Thur, 6 Mar, 8:20pm
8	F	7-Mar			
9	М	10-Mar	4		
9	Tu	11-Mar	4	25-26	Exam 1 Solutions / Regrade petitions submitted
9	W	12-Mar	4		ASM implementation, ASM design examples
9	Th	13-Mar	4	27	ASM designs, ROM based designs & others
9	F	14-Mar	4		RAM/ROM expansion
		15-Mar			Spring Break (no classes or labs)
		→23-Mar			
10	М	24-Mar	5		ASM design implementations
10	Tu	25-Mar	5	28-29	Introduction to VHDL
10	W	26-Mar	5		RAM, ROM PLAs, PALs
10	Th	27-Mar	5	30	More PLDs (CPLDs and FPGAs)
10	F	28-Mar	5		
11	М	31-Mar			Introduction into computer architecture
11	Tu	1-Apr		31-32	Addressing modes, Data transfer instructions
11	W	2-Apr			
11	Th	3-Apr		33	
11	F	4-Apr			
12	М	7-Apr	6		Basic computer operation cycles and timing
12	Tu	8-Apr	6	34-35	Instruction set & assembly programming examples
12	W	9-Apr	6		
12	Th	10-Apr	6	36	
12	F	11-Apr	6		Drop Deadline: Fri, 11 Apr @ 11:59pm
13	М	14-Apr			G-CPU, Special topics
13	Tu	15-Apr		37-38	
13	W	16-Apr			Practical 2: Wed, 16 Apr, 8:20pm
13	Th	17-Apr	7	39	
13	F	18-Apr	7		
14	М	21-Apr	7		G-CPU, Memory Maps
14	Tu	22-Apr	7	40-41	
14	W	23-Apr	7		
14	Th	24-Apr	No lab	No class	Reading Day
14	F	25-Apr	No lab	No class	Reading Day
Sat		26-Apr		Final Exam	Final (Exam 2): TBD

University of Florida Department of Electrical & Computer Engineering Page 13/13

Ronan Morris wrote a draft procedure for us to use checking the DE10-lites. He also wrote a PowerShell script to make it a little easier than manually programming using three different object files (.sof/.pof).

He could not see any way of resetting to the default program (i.e. without programming). He also didn't see any easy way to check anything besides the LEDs and programming success, but at least these are the most common issues.

He <u>uploaded the folder to Github</u> to make it easy to read the instructions in the markdown file format. See the instructions in readme.md. A zip of the instructions is available at <u>https://mil.ufl.edu/3701/software/OC DE10.zip</u>. Screenshots of the process, object files used for programming, and scripts to make programming easier.