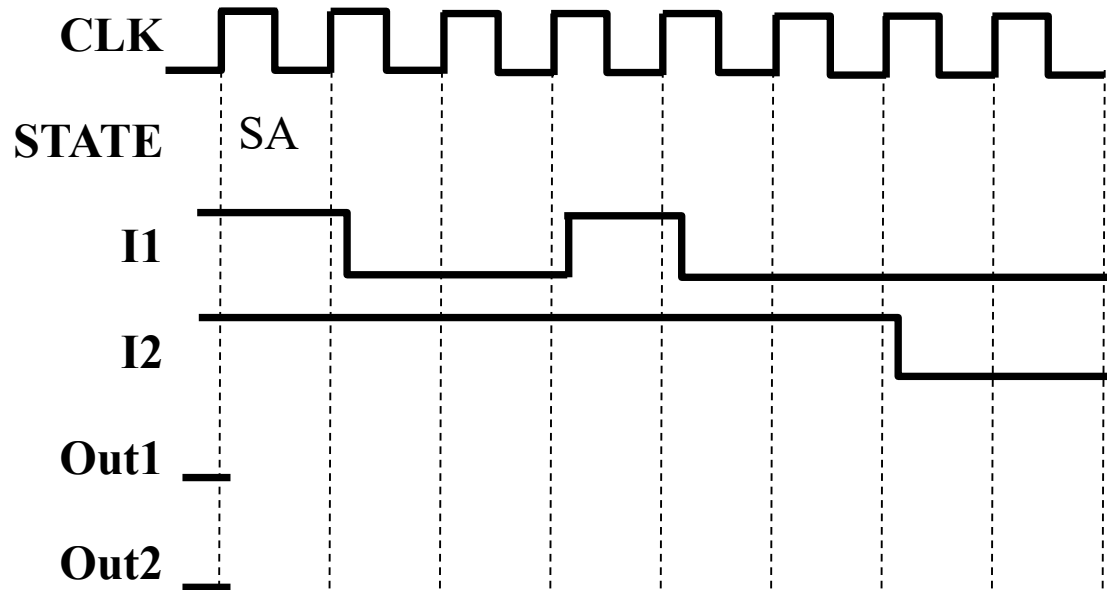
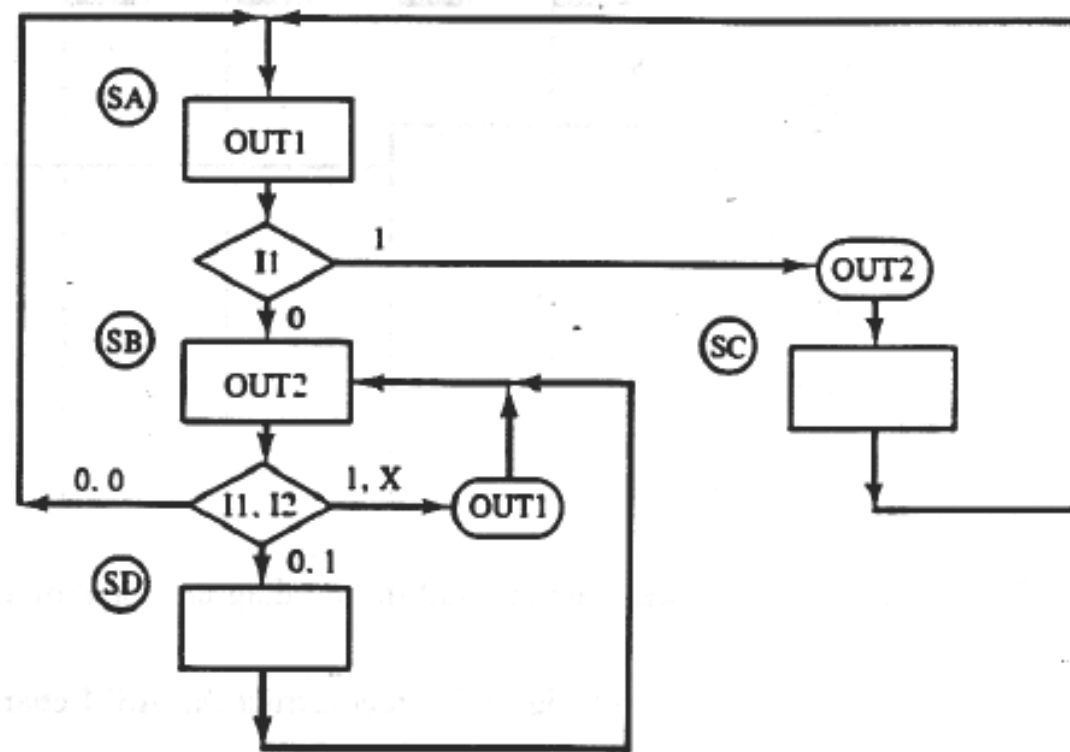
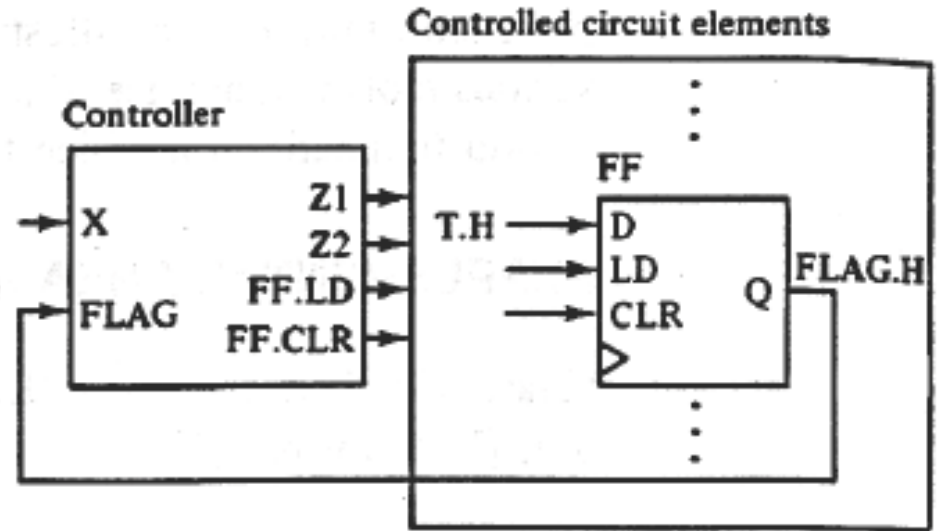
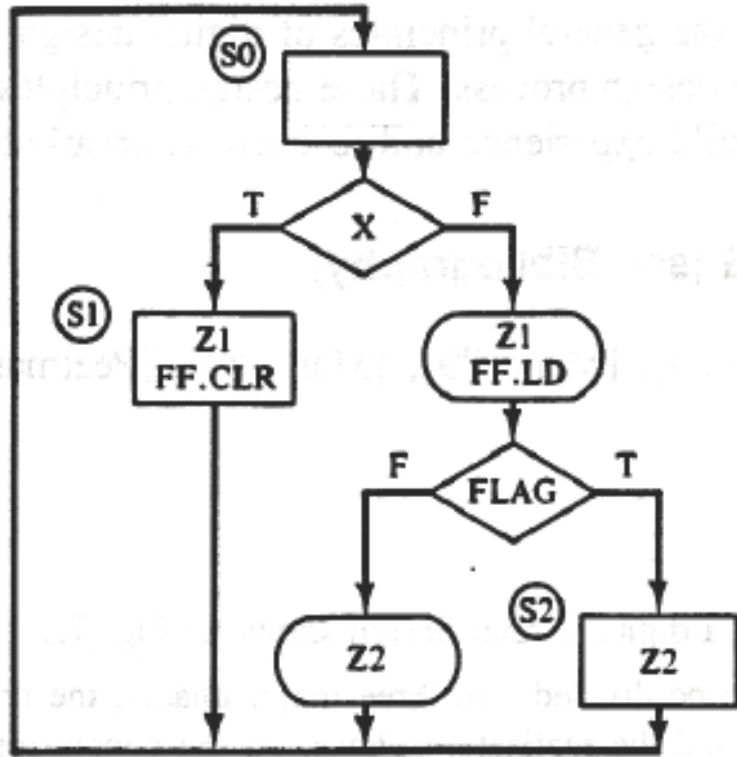


Problem 7.3 (Lam)

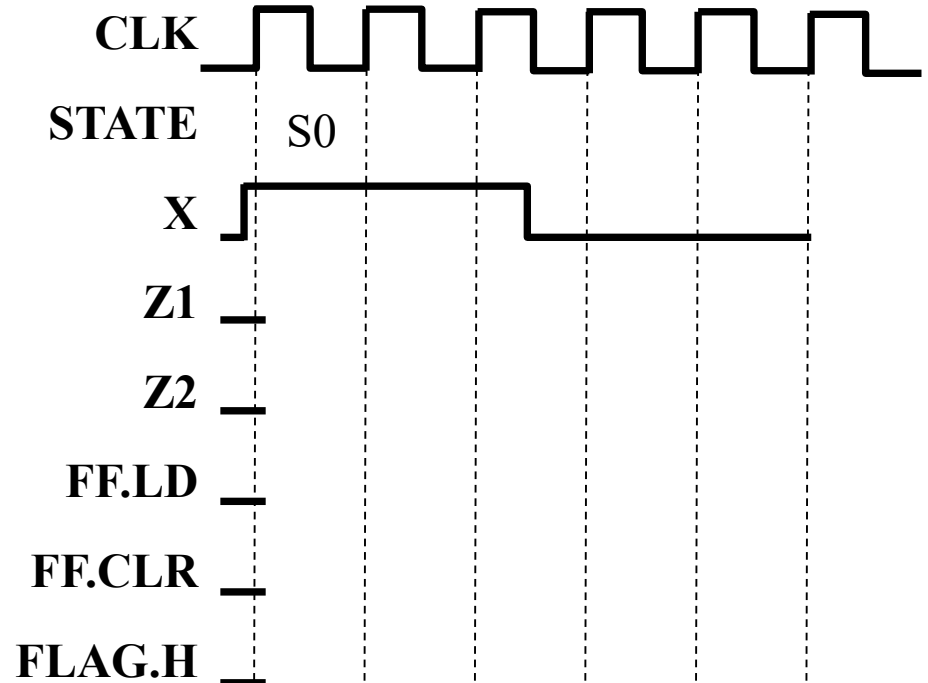
Given this ASM, complete the timing diagram.



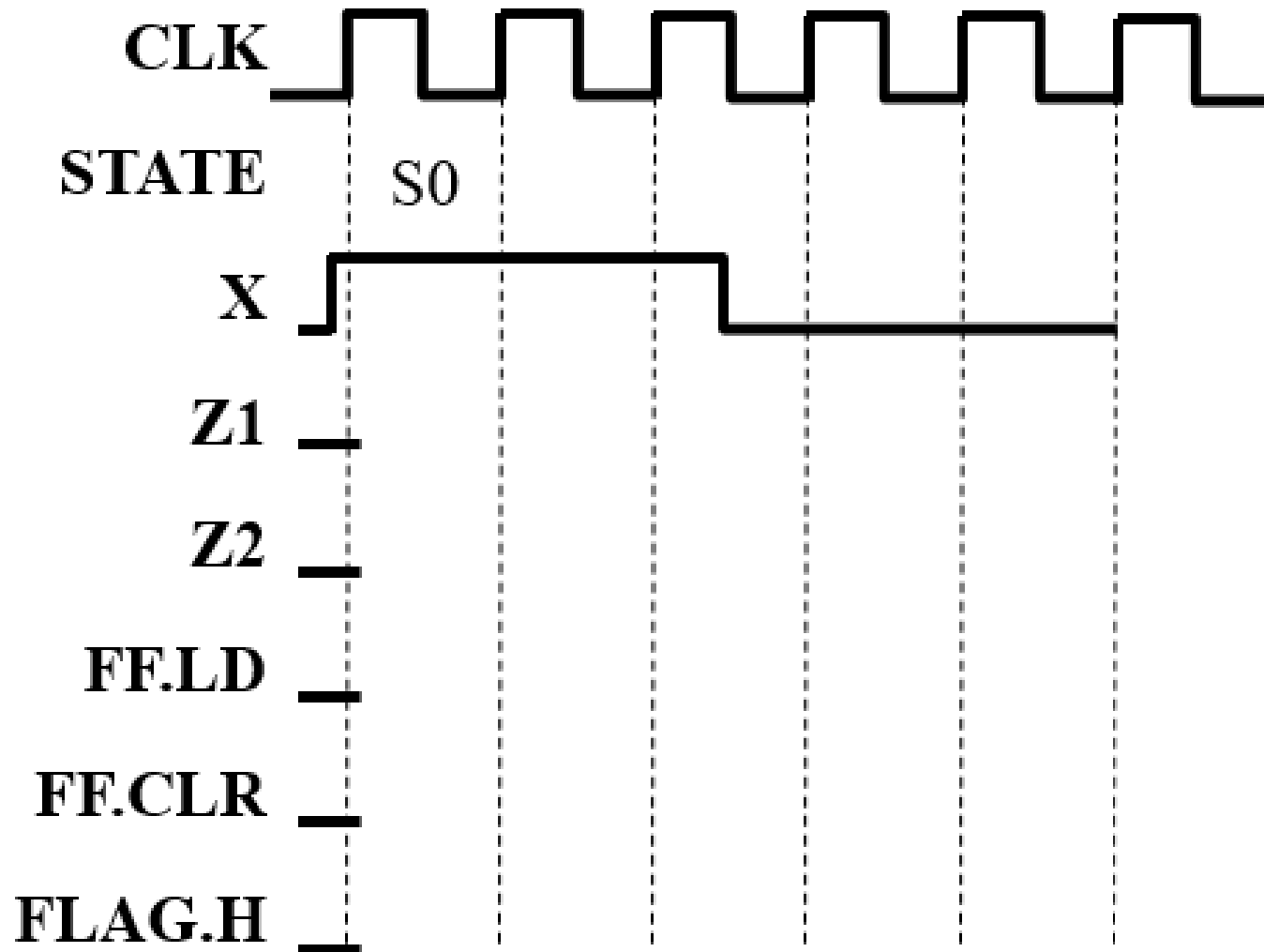
Problem 7.4 (Lam): Given this ASM & block diagram, complete the timing diagram.



Note: The FF has a **sync** load and **sync** clear.

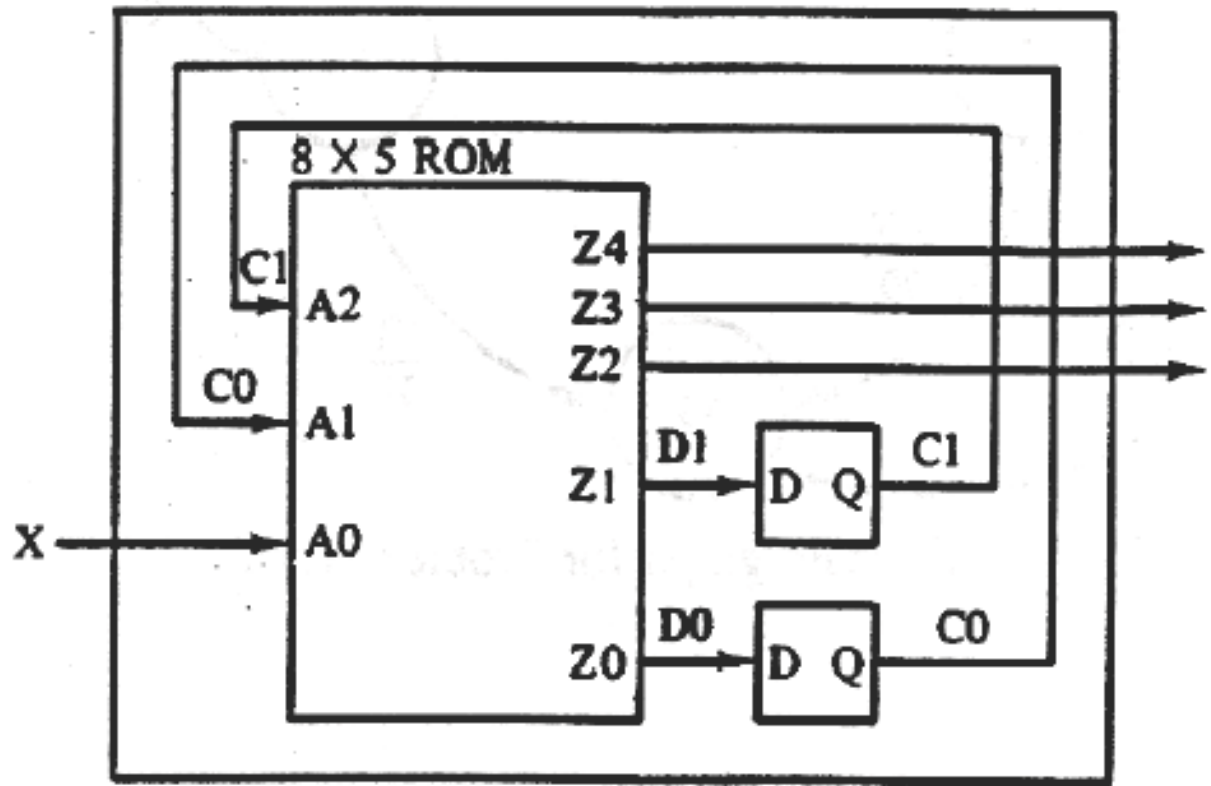


Problem 7.4 (Lam): Timing diagram.



Problem 7.15 (Lam):

Below is a block diagram and the contents of a ROM. Derive the corresponding ASM.



Contents of the ROM	
Location	Contents
0	11H
1	11H
2	16H
3	0FH
4	00H
5	00H
6	04H
7	04H