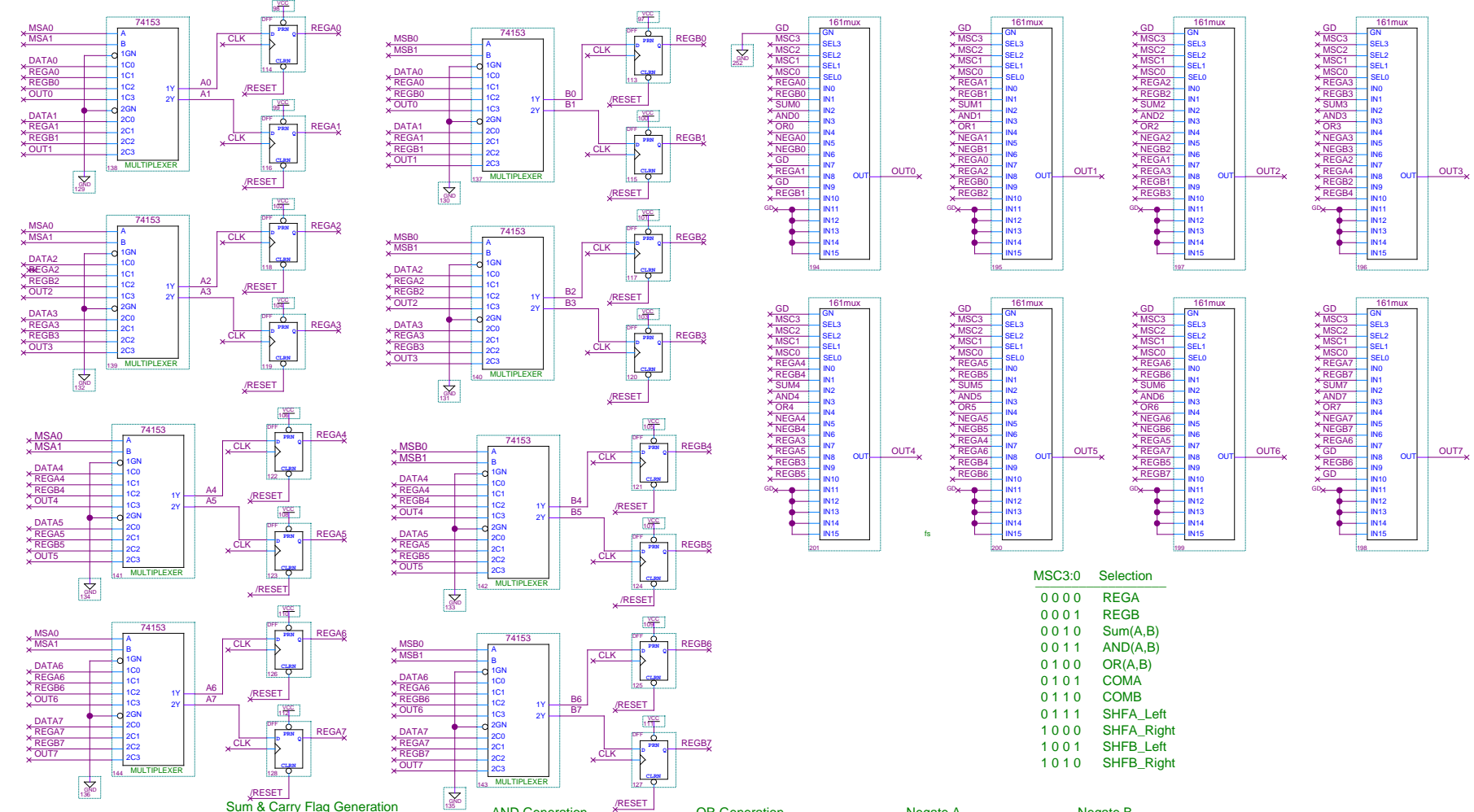
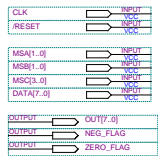


Mux A, Mux B, Reg A, Reg B Block

Mux C Block



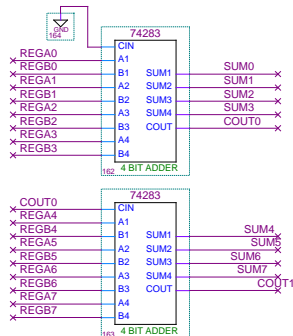
Input & Output Signals



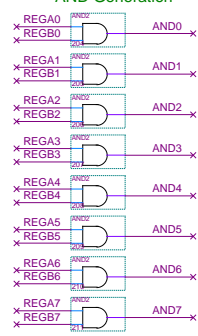
Debug Signals



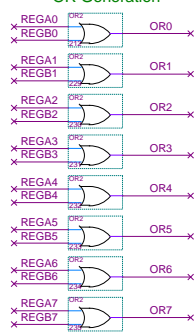
Sum & Carry Flag Generation



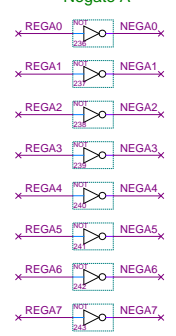
AND Generation



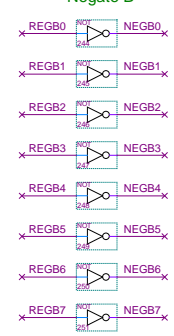
OR Generation



Negate A



Negate B



Z & N Flag Generation

