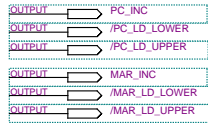
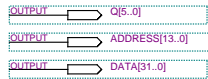


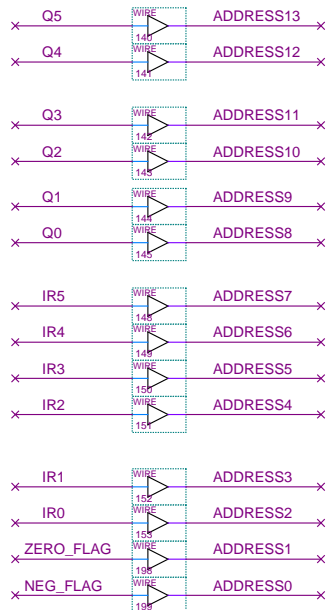
## Controller Input &amp; Output



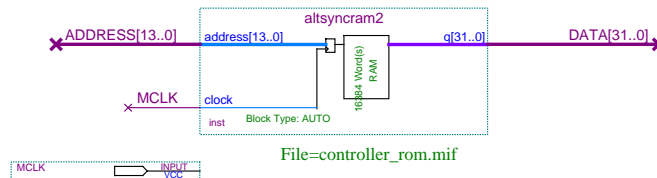
## Debug Purposes



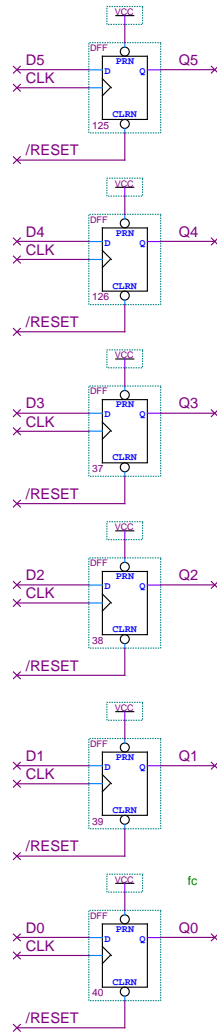
## Controller Inputs (State, Flags, Instruction)



## Controller Logic



## ASM State Generation



## Controller Outputs

