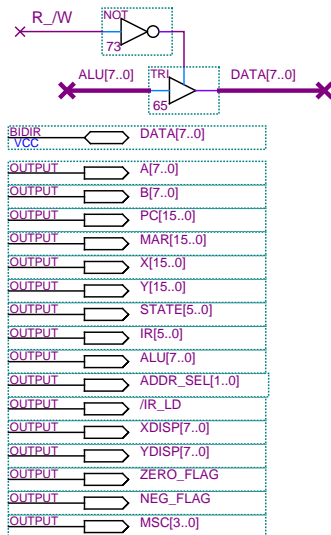
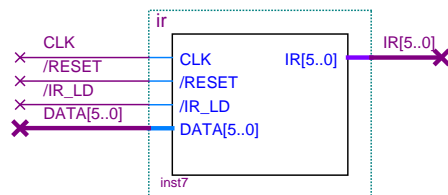


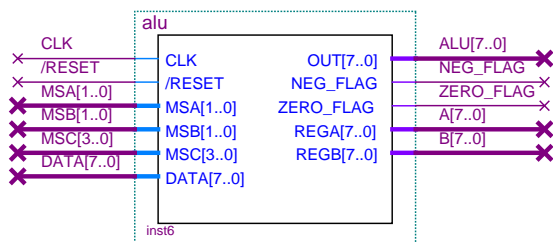
Data Bus Tri-State Creation



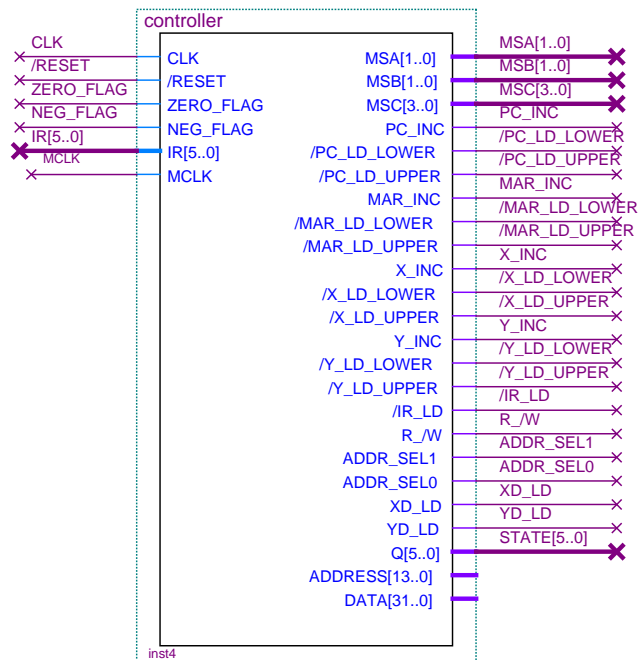
Instruction Register (IR)



Arithmetic Logic Unit (ALU)



CPU ASM Controller



Program Counter (PC) & Memory Address Register (MAR) & Index Regs (X,Y)

