

AAND2

Data Sheet
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AAND2

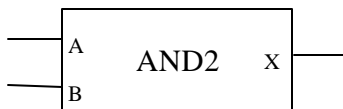
Features

- AND function
- 2 inputs
- 1 output

Functional Behavior

A	B	X
0	0	0
0	1	0
1	0	0
1	1	1

Drawing Symbol



Include

```
library ieee;  
use ieee.std_logic_1164.all;
```

Code (AAND2.vhd)

```
library ieee;  
use ieee.std_logic_1164.all;  
entity AAND2 is port(  
    A,B: in std_logic;  
    X: out std_logic);  
end AAND2;  
  
architecture behavior of AAND2 is  
begin  
    X <= A and B;  
end behavior;
```

Entity

```
entity AAND2 is port(  
    A,B: in std_logic;  
    X: out std_logic);  
end AAND2;
```

Component

```
component AAND2 port(  
    A: in std_logic;  
    B: in std_logic;  
    X: out std_logic);  
end component;
```

Port Map

```
U_: AAND2 port map(  
    A => ____,  
    B => ____,  
    X => ____);  
U_: AAND2 port map(A,B,X);
```