Speedy



by Josue Peña

Keith L. Doty EEL 5666 Intelligent Machines Design Laboratory

> University of Florida December 11, 1997

Table Of Contents

Abstract	3
Executive Summary4	1
Introduction	5
Integrated System	5
Mobile Platform	7
Actuation	3
Sensors 1	10
Behaviors1	14
Experimental Layout and Results1	16
Conclusion1	19
Documentation2	20
Appendix A: Behavior Code	21

Abstract

Following is a discussion of the development of an obstacle avoidance system for an autonomous high speed RC car using infrared emitters and sensors. The car, known as Speedy, has two basic modes of operation. Speedy can operate in autonomous mode or full radio control mode. Speedy's high speed requires unique circuitry to power the high current motor. In addition the radio control mode is complicated by RF noise produced by the motors and the micro-controller which dictates Speedy's behavior.

Executive Summary

Speedy is an autonomous car designed to travel at high speeds. A powerful rear motor and differential gears provide good traction at high speeds. This motor is controlled and powered by a special high current electronic speed control. A high current delivering battery is used to operate at fast speeds with long run time. The original steering mechanism was replaced with a standard servo and torque coupling to provide tighter steering and reliable control.

Speedy's navigational system consists of IR emitters and receivers. Since Speedy will travels at high speeds, obstacle avoidance becomes a challenge. Upon detecting an obstacle Speedy will reduce its speed while adjusting the steering to avoid the obstacle. Speedy will reverse the motor for a fraction of a second if necessary to slow down. The integrated IR system can provide 140 degrees of frontal visibility up to 24 inches ahead. Even though an IR sensor is located on the back of Speedy, rear visibility is not a priority since Speedy will generally travel in the forward direction.

Speedy can also run in full radio control mode. Upon receiving valid RF signals from an FM transmitter, Speedy will switch from autonomous mode to full radio control mode. Upon losing the RF signal Speedy will switch back to full autonomous mode. Speedy can switch between both modes as often as desired.

Speedy uses the Motorola 68HC11 micro-controller to analyze all sensors and then control the speed and direction accordingly.

Introduction

My primary goal for Speedy is to be able to switch to radio control automatically and then maintain radio control at a reasonable distance. Prior RC designs have been unsuccessful due to EMF noise produced primarily by the Motorola 68HC11 micro-controller. Speedy can operate in two modes which are selected automatically. Mode one is full RC control. One will be able to control Speedy's movements from a far distance (about 50 feet). Mode two is full autonomous mode. In this mode Speedy will roam around the room avoiding obstacles looking busy. Speedy will increase speed whenever he detects that there is enough room to navigate in safely without crashing.

Following are separate sections describing Speedy. The *Integrated System* section gives an overview of the separate systems which makes Speedy come to life. The *Mobile Platform* section describes the physical structure of Speedy and what makes it moves around. The *Actuation* section discusses the steering mechanism and throttle mechanism. The *Sensors* section lists and describes the different sensors used in determining Speedy's direction and speed of travel. The *Behaviors* section describes the behaviors and modes which Speedy exhibits. The *Experimental Layout and Results* section discusses the experiments and results conducted in getting Speedy to work properly. The *Conclusion* section is a brief summary of what was accomplished and what was not accomplished. The *Documentation* section lists the books and papers used as a reference or guide while designing Speedy. Finally, the Appendix A contains the behavioral code which makes Speedy run around looking important.

Integrated System

Speedy's control system is a rather simple one as shown in Figure 1 below. Speedy first initializes the control of the steering and motor. This initialization makes sure Speedy does not ram into an obstacle at startup. Speedy then cycles through four processes continuously. The first process calculates which direction Speedy should go. The second process determines at what speed to travel in. The third process determined whether to be in autonomous or full radio control mode. Finally, the fourth process is the arbitrator which analyzes all the information generated by the other processes and determines what Speedy will actually do.



Figure 1: Control Flow

The autonomous steering and the autonomous speed data are calculated directly from the IR sensor readings. When the arbitrator is invoked it actuates the steering and throttle mechanisms which moves the motors. When in radio control mode the arbitrator simply passes the signals coming in from the receiver to the steering servo and the electronic speed control.

Mobile Platform

Due to Speedy's fast speed a strong and crash resistant platform is needed. The "Black Phantom II" RC truck by Radio Shack was therefore used. The rear bumper was removed and the top half of the front bumper was removed. Only the frame, wheels assembly with shocks, front bumper, and rear motor assembly were kept. The steering steeper motor was removed because it did not provide the proportional steering needed for good obstacle avoidance. The battery and battery casing were removed because it was too small to provide enough power for a successful run. The front bumper was trimmed low in order to increase the IR sensor range. The rear bumper was removed because it was too heavy and dragged occasionally slowing Speedy down. Finally the RF electronics were removed because they were cheaply designed and very prone to interference. After many crashes (mostly accidental) Speedy's frame has proven to be very reliable and sturdy. The excellent forward and floating rear suspension maintains Speedy from flipping over is mostly any driving condition. Since Speedy does not have a rear bumper a rear collision can cause damage to the rear motor assembly. This is not a major concern since Speedy mostly travels in a forward direction and is allowed to travel only slowly in reverse.

Actuation

Front Steering Mechanism

Speedy's original steering mechanisms was very problematic. The wheels were turned left or right by applying a voltage to the appropriate coil on the steeper motor. The range of motion was very limited and very hard to control in this design. The steeper motor was replaced with a standard servo form the IMDL lab. A couple modifications to the car body were needed to accommodate and secure the new servo. The torque coupling from the old motor was removed and attached to the new servo. This torque coupling allows the wheels to give a little if the turn is too tight. This has a couple of advantages. First, if the wheels are stuck then the servo won't overheat trying to turn the wheels. The servo will simply turn and the torque coupling will give a bit. The second advantage is that when making a sharp turn this little bit of give keeps the car from flipping over. The only disadvantage is that the tighter the curve the wider the turn. Even with these modifications Speedy still has a wide turning diameter of 20 inches as shown in Figure 2 below. This wide turning radius is due to Speedy's over sized tires and wide axles.



Figure 2: Turning Diameter

Rear Drive Motor

Speedy rear wheels were originally powered by a 9.6 volt bi-directional DC motor. This motor is connected to a differential gear assembly which delivers good traction on sharp turns and different road surfaces. The motor typically draws 0.45 Amps at maximum speed. The stall current of the motor is around 11 Amps. This high stall current requires a special electronic speed control discussed in the sensor section of this report. Even though this motor originally was powered with a 9.6 volt battery pack, it is now powered with a 7.2 volt Piranha battery pack. This was done to meet the electronic speed control input voltage maximum and also because this battery pack can supple twice the original power (1.4 Amp-Hrs). In order to accommodate this larger battery pack the bottom battery compartment was widened and the new battery was fastened using bell wire and electrical tape.

<u>Sensors</u>

Sharp IR Detectors

Speedy uses IR emitters and detectors as it's main form of obstacle avoidance. There are four Sharp IR sensors located on Speedy. Three in front and one in the back as shown in Figure 3 below. Directly below these sensors are four IR emitters calumniated inside a black tube. In addition to these four emitters there are four uncalumniated emitters. Two of the emitters point forward and two point to each side. These last four emitters were added experimentally to improve Speedy's IR blind spots on it's sides. The IR emitters are connected in series with a 330 ohm resistance to the ME11 board which produces a 40kHz signal suitable for the Sharp sensors.



Figure 3: Sharp IR Sensors

These IR emitter/sensor combination gives Speedy 24 inches of obstacle detection. This IR short range is a problem at high speeds because obstacle avoidance is sometimes unavoidable. Future modifications might include ultrasonic emitters and detectors to increase range.

Bump Sensors

Speedy was equipped with a couple of bump sensors behind a fender attached to the front bumper. This enabled Speedy to determine when a low or dark obstacle not detectable by IR was in its path. After a couple of test runs and crashes, the bumper shattered. Therefore, the bump sensor no longer plays a role in obstacle avoidance.

Electronic Speed Control

A Traxxas XL-1 electronic speed control (ESC) is used to in order to power Speedy's high speed motor. This speed control can with stand stall currents of about 75 Amps. This speed control consists mainly of two rows of 8 MOSFETS in parallel. Even thought this electronic speed control (ESC) is very powerful it has one major disadvantage in its control. If this ESC does not receive a 20ms period waveform with a pulse width between 1.2 and 1.8 ms it will start oscillating. Once the ESC starts to oscillate, the motor as well as the speed control begin to overheat. Table 1 below shows how the ESC responds to pulse widths between 1.2ms to 1.8 ms.

Throttle	Pulse Width (ms)	68HC11 FRC Counts
Maximum Forward	1.20	2400
	1.24	2480
	1.28	2560
	1.32	2640
	1.36	2720
	1.40	2800
	1.44	2880
Stopped	1.48	2960
Stopped	1.52	3040
	1.56	3120
	1.60	3200
	1.68	3360
	1.72	3440
	1.76	3520
Maximum Reverse	1.80	3600

 Table 1: Throttle Control

The 68HC11 FRC column indicates how long the free running counter on the 68HC11 must run to produce the desired pulse width.

The electronic speed control power line was tapped to power the steering servo and the FM receiver. A resistor is placed in between the electronic speed control and the 68HC11. This resistor serves two purposes. The first purpose of the resistor is to reduce the signal voltage to about 4.2 volts which is the expected signal input voltage of the ESC. The second purpose is to reduce voltage spikes cause by the motor. When observing the signal on the oscilloscope a spike is visible feeding into the 68HC11 output compare pin. After installing this resistor the spike disappeared. With out this resistor the speed control does not work, and will begin to oscillate. This resistor caused two months of trouble shooting and patience to figure out.

Futaba RF Receiver

Speedy originally came with a Radio Shack 27 MHz AM radio and receiver. The maximum control range was about 30 feet. After applying power to the 68HC11 complete control of Speedy was lost. This lost of control was due primarily for two reasons. First, the 68HC11 micro-controller emits a lot of RF/EMF noise which floods out the radio signal. Second, the cheap Radio Shack receiver used, responds to a wide band and doesn't zero in on the 27MHz signal. A Traxxas 27 AM MHz radio and receiver from an old Grasshopper RC car was then tried. With this receiver Speedy can be controlled from 50 feet. Once again after applying power to the 68HC11 micro-controller 5 second continuous glitches occurred. These glitches usually leads to Speedy ramming into a wall.

Finally a FM model airplane receiver was used. This receiver is a Futaba FP-R127DF FM 7channel receiver. The crystals were changed from Channel 44 (76.670 MHz) to Channel 52 (72.830 MHz) to eliminate RF noise bled on by local radio stations. The Futaba receiver has a narrow response band of only 20kHz. This receiver gives Speedy an RF range of about 50 feet with very few glitches.

Custom Sensor Interconnections

The major components making up the customs sensors is shown Figure 4 below. The 68HC11 receives data from the electronic speed control and the radio receiver. The rear wheel motor is powered by the electronic speed control which is under direct control of the 68HC11. The steering servo is powered by the radio receiver and is under direct control of the 68HC11 also.



Figure 4: Sensor Wiring

Behaviors

Obstacle Avoidance

Speedy's high speed and high turning radius makes it hard to avoid obstacles. The A/D converters are put in multiple channel scanning mode to provide up to date IR data. Speedy analyzes the difference between the left and right sensors to determine how hard and which way to steer. If the difference is not significant then Speedy will remain in its current course. This prevents Speedy from jerky steering. Due to Speedy's high speed, obstacle avoidance was not easy and not yet full proof.

Maximum Speed Navigation

Speedy relies heavily on the IR sensor reading to determine his speed. Depending on how much headway room there is in front of Speedy he will roam at different speeds. If Speedy is going down a tight passage then he will set his throttle to the slowest speed possible. Setting the speed accordingly will give Speedy enough time to turn and avoid obstacles. If Speedy goes into a tight passage way which is too tight he will simple stop. Since speedy only has one sensor in the back he cannot travel in reverse and get out of a windy situation. However if Speedy is approaching an obstacle at a high speed he will apply the reverse (unless the rear is blocked) to slow him down enough to clear the turn.

Automatic Radio Mode

Speedy is constantly using the RF receiver to monitor the airways for any valid data. Whenever Speedy determines that there is valid RF data coming in he switches over from autonomous mode to full radio control mode. Whenever Speedy stops receiving a good signal he sets a timer and waits for about 30 ms. If another good RF signal does not come in then Speedy switches over to autonomous mode. Due to all the noise produced by the motor and the 68HC11 micro-

controller the RF signal coming in varies a bit. Speedy determines if the signal is good by checking the period of the signal and also the duty cycles of the signals coming in. Since this will vary a bit due to noise and other factors such as the transmitter/receiver distance Speedy does not compare it to a fixed number but makes sure it's within certain limits. By adjusting these limits most glitches can be eliminated. If one makes these limits too tight them all glitches will be eliminated but then Speedy will never switch into radio control mode.

Radio Mode with Autonomous Override

Due to time constraints and RF problems I was not able to implement this mode successfully. It's goal was to be able to maneuver Speedy remotely and still leave some autonomous routines. If I were to ram into a wall then Speedy should of either stopped or turned to avoid the wall. Also if I were to travel at a higher speed than Speedy thought was safe to maneuver in, then he would of reduced the speed. The major setback in implementing this mode was in the ability to enable it remotely with the RF radio. Even though the radio/receiver has an extra channel to send data over when I tried to send data, there was too much noise to make sense of it. I believe that this is due to the way I am powering the RF receiver.

Experimental Layout and Results

IR Testing

The first experiment conducted on Speedy was too get some base data for the IR sensors. All the IR emitters were turned on and then an object was placed in front of each of the IR sensors at different distances ranging from 24 inches to 0 inches. After the object was placed in front of the emitter and the distance was measured, the appropriate sensor output voltage was measured using the 68HC11's analog-to-digital converter. The data readings from the A/D converter is shown below in Table 2.

Distance	Analog 1 (Left)	Analog 1 (Center)	Analog 2 (Right)	Analog 3 (Rear)
0	129	130	130	130
1	130	130	129	130
2	130	131	128	130
3	129	130	124	130
4	126	127	123	126
5	122	123	119	123
6	118	120	118	119
7	113	117	115	116
8	108	113	112	112
9	104	110	108	107
10	99	106	104	104
11	96	102	100	101
12	93	98	97	98
13	91	96	94	95
14	90	94	92	93
15	88	93	91	93
16	87	92	89	92
17	87	90	88	92
18	86	89	87	91
20	86	88	86	91
21	86	87	86	91
24	86	86	86	91

 Table 2: IR Sensor Data

As can be seen by the IR sensor data plot, shown in Figure 5 below, all four of the IR sensors readings have a similar format. Between 5 and 13 inches the sensors are relatively linear. Below 5 inches and above 13 inches the sensors begin to exponentially saturate at a given voltage value.



IR Sensors

Figure 5: Analog IR Readings

Different resistors were tried in series with the IR emitters. Resistors ranging from 10 ohms to 500 ohms were used. After testing various resisters a resistance of 330 ohms produced the maximum range. For some reason, resistors greater than 330 ohms seemed to decrease the range of the IR emitter/sensor combination. This lack of resistance in the latch output introduced some noise in the A/D system which hindered consistent readings. Therefore, a 330 ohm resistor pack was placed on the latch output for the IR emitters.

RF Testing

Testing the RF components was a bit tricky. The first test was to reduce RF noise emissions. This was done mostly by trial and error. Every time a new cable was added, the RF signal was observed on the oscilloscope and checked for noise. Simple things such as long power wires caused noise in the RF signal. Another thing that eliminates RF noise was using shielded wire to power the 68HC11. After much of the RF noise was eliminated, observation of the actual RF signal was done. Even though the signal appears pretty constant on the oscilloscope, as the distance between Speedy and the RF transmitter changes the RF signal varies slightly. Different periods and pulse widths were recorded as Speedy moved along in radio control mode. Even though an ideal RF signal has a period of 20 ms and pulse width between 1 ms and 2 ms this is not usually the case. Therefore waveforms with a period of 20 ms \pm 0.05 ms and pulse widths between 2.025 ms and 0.975 ms are considered good and valid.

Electronic Speed Control (ESC) Testing

The electronic speed control had one problem which took a lot of time to figure out. Even though the ESC takes in a standard servo signal, it did not work and began to oscillate. After many trials and error I determined that a resistor between the 68HC11 output compare and the ESC was needed. Once again different resistor values were tried. Even though a resistance value of 3000 ohms stabilized the ESC and made it usable when I observed the output compare signal on the oscilloscope I noticed a faint voltage spike on the center of the waveform. When I increased the resistance to 6600 ohms the voltage spike went away. As a result of the added resistance, when the motor is stopped is does not turn on or off as harsh and noisy as it did with the 3000 ohm resistor.

Conclusion

Building Speedy has taught me many things. The most important lesson I learned is the difference between theory and reality. In a theoretical or ideal world it's easy to design a robot. In an actual world sensors, motors, and devices work very differently. All the non-ideal characteristics of motors and sensors complicate the designing of a robot. Speedy is now a high speed autonomous car which avoids obstacles at high speeds. In addition to begin fully autonomous Speedy can switch automatically to radio control mode upon receiving a valid RF signal.

The current distance limitations of the IR sensors limit Speedy's performance in obstacle avoidance. Future designs of this robot will include more powerful proximity sensors such as ultrasonic transducers. Future design of this robot will also include a third mode which mix autonomous mode with radio control mode.

Documentation

Books and Papers

- [1] Joseph Jones & Anita Flynn, <u>Mobile Robots: Inspiration to Implementation</u>, A.K. Peters Publishers, Wellesley, MA, 1993.
- [2] Gene Miller, Microcomputer Engineering, Prentice-Hall Inc., Englewood Cliffs, NJ, 1993
- [3] Fred Martin, "The 6.270 Robot Builder's Guide", The MIT Press, c.1992

Appendix A: Behavior Code

* 68HC11 Regi	isters		
TIC2	EQU	\$1012	; Timer Input Capture 2 Register
TIC3	EQU	\$1014	; Timer Input Capture 3 Register
TOC2	EQU	\$1018	; Output compare 2 register
TOC3	EOU	\$101A	; Output compare 3 register
TOC4	EÕU	\$101C	; Output compare 4 register
TCTL1	EOU	\$1020	; Timer control register 1
TCTI.2	EOU	\$1021	: Timer control register 2
TMCK1	FOII	\$1022	: Timer magk register
	EQU	¢1022	· Timer flag register
	EQU	\$1023 ¢1000	, Timer Hay register
	EQU	\$100E	, limer counter Register
SCSR	EQU	ŞIUZE	; Serial communication status register
SCDR	EQU	\$102F	; Serial communication data register
OPTION	EQU	\$1039	; Hardware option control register
ADCTL	EQU	\$1030	; A/D control register
ADR1	EQU	\$1031	; A/D first result register
ADR2	EQU	\$1032	; A/D second result register
ADR3	EQU	\$1033	; A/D third result register
ADR4	EOU	\$1034	; A/D fourth result register
BAUD	EOU	\$102B	; Baud Rate Register
SCCP1	FOII	\$1020	; SCI Control 1 Register
SCCKI	FOU	\$102C	; SCI Control 2 Register
SCCRZ	шQU	ŞIUZD	/ Ser concror z Register
* Masks			
BIT0	EQU	%00000001	; Bit 0 mask used to isolate bit
BIT1	EOU	%00000010	; Bit 1 mask used to isolate bit
BIT2	EÕU	%00000100	; Bit 2 mask used to isolate bit
BTT3	EOU	<u>%00001000</u>	; Bit 3 mask used to isolate bit
BIT4	FOII	\$00001000 \$00010000	: Bit 4 mask used to isolate bit
	EQU	%00010000 %00100000	, Bit F mask used to isolate bit
BIIS	EQU	300100000	, BIL 5 MASK USED LO ISOIALE DIL
BT.1.0	EQU	%01000000	; Bit 6 mask used to isolate bit
BIT'/	EQU	\$10000000	; Bit / mask used to isolate bit
IBIT0	EQU	%11111110	; Bit 0 inverse mask used to isolate bit
IBIT1	EQU	%11111101	; Bit 1 inverse mask used to isolate bit
IBIT2	EQU	%11111011	; Bit 2 inverse mask used to isolate bit
IBIT3	EOU	%11110111	; Bit 3 inverse mask used to isolate bit
IBIT4	EOU	%11101111	; Bit 4 inverse mask used to isolate bit
TBTT5	EOU	&11011111	; Bit 5 inverse mask used to isolate bit
твттб	FOII	\$10111111	: Bit 6 inverse mask used to isolate bit
	EQU	010111111 001111111	· Bit 7 inverse mask used to isolate bit
IDII/	ЕQU	001111111	, bit / inverse mask used to isolate bit
PERIOD	EQU	40000	; 20ms PWM period for servo & ESC
STRAIGHT_PWM	EQU	3000	; Straight PWM (3000)
SOFTLEFT_PWM	EQU	3500	; Soft Left PWM (3500)
SOFTRIGHT_PWN	4 EQU	2500	; Soft Right PWM (2500)
MEDLEFT_PWM	EQU	3750	; Medium Left PWM (3750)
MEDRIGHT_PWM	EQU	2250	; Medium Right PWM (2250)
HARDLEFT PWM	EOU	3875	; Hard Left PWM (3875)
HARDRIGHT_PWN	4 EQU	2125	; Hard Right PWM (2125)
		2000	
S.T.Ob bMM	EQU	3000	; Stopped PWM (3000)
MAXFRW_PWM	EQU	2500	; Maximum Forward Speed PWM (2500)
MAXREV_PWM	EQU	3500	; Maximum Reverse Speed PWM (3500)
MEDFRW_PWM	EQU	2650	; Medium Forward Speed PWM (2750)
MEDREV_PWM	EQU	3350	; Medium Reverse Speed PWM (3250)
SLOWFRW_PWM	EQU	2800	; Slow Forward Speed PWM (2900)
SLOWREV_PWM	EQU	3250	; Slow Reverse Speed PWM (3150)
	TOT	07	· TD Three held
I_IK	EQU	δ/ 10	, IK INTESNOIQ
S_DIFF	EQU	TU	; Small IR difference
M_DIFF	EQU	20	; Medium IR difference
L_DIFF	EQU	30	; Large IR difference

CS	EQU	0	;	Collision straight
CHL	EQU	1	;	Collision hard left
CHR	EQU	2	;	Collision hard right
CML	EQU	3	;	Collision medium left
CMR	EQU	4	;	Collision medium right
CSL	EQU	5	;	Collision soft left
CSR	EQU	6	;	Collision soft right
CSTOP	EQU	0	;	Collision stop
CMAXF	EQU	1	;	Collision maximum forward
CMAXR	EQU	2	;	Collision maximum reverse
CMEDF	EQU	3	;	Collision medium forward
CMEDR	EQU	4	;	Collision medium reverse
CSLOWF	EOU	5	;	Collision slow forward
CSLOWR	EOU	6	;	Collision slow reverse
	~ -			
	ORG	\$100		
TOTAL	RMB	2		
	10.12	_		
PWMSERVO	RMR	2	:	Pulse width for steering servo
DWMESC	PMB	2	;	Pulse width for electronic speed control
(FCC)	RHD	2	'	raise widen for creectonic speca control
	DMD	2		Dulgo with from radio regainer for starring
PWMRSERVO	RMB	2	΄.	Pulse width from radio receiver for Sceering
PWMRESC	RMB	Z	'	Pulse width from fadio fecerver for ESC
		2		Test times innut contume 2 melus
LAST_TIC2	RMB	2	'	Last timer input capture 2 value
LAST_TIC3	RMB	2	;	Last timer input capture 3 value
		-		
COLLISION_DIR	RMB	1	;	Collision Direction
COLLISION_SPEED	RMB	1	;	Collision Speed
RADIO_MODE	RMB	1	;	Are we in radio controlled mode?
L_TIC2	RMB	2		
REVTIME	RMB	1	;	Time to stay in reverse
* Interrupt Vect	tors			
	ORG	\$FFE6	;	Output Compare 2 Interrupt Vector
	FDB	OC2ISR		
	ORG	\$FFE4	;	Output Compare 3 Interrupt Vector
	FDB	OC3ISR		
	ORG	\$FFE2	;	Output Compare 4 Interrupt Vector
	FDB	OC4ISR		
	ORG	\$FFEC	;	Input Capture 2 Interrupt Vector
	FDB	IC2ISR		
	ORG	ŚFFEA	;	Input Capture 3 Interrupt Vector
	FDB	IC3ISR		
	100	100101		
	ORG	ਸ਼ਰਤਟ	;	Reset Vector
	FDB	START		
		S 1111(1		
* Start of prog	ram			
DEALE OF PLOG	- am			
	ORG	\$8000	;	Starting RAM Address
	510	Y 0000	'	Startering fain flaarebb
START	lda	#\$47	;	Initialize stack pointer
₩1111(1	ldaa	#%10100000	;	Set OC2 & OC3 pin to low
		1		200 000 a 000 pin co iow

ldaa	#%10100000	;	Set OC2 & OC3 pin to low
staa	TCTL1	;	on successful compare
ldaa	#%00000101	;	Initialize IC2 & IC3 for
staa	TCTL2	;	low-to-high capture
ldaa	#%01110011	;	Enable OC2, OC3, OC4,
staa	TMSK1	;	IC2 and IC3 interrupts

	ldaa	#\$30 BAUD	;	baud = 9600	
	clr ldaa	SCCR1	;	1 start 1 stop 8 data bits	
	staa	SCCR2	;	enable Tx and Rx	
	ldaa staa	#\$FF \$7000	;	Turn on IR emitters	
	cli		;	Turn on interrupts	
	ldaa	#BIT7	;	Power-Up A/D using E-clock	
	staa ldaa staa	OPTION #%00110000 ADCTL	; ;	Scanning multiple channels START A/D CONVERSION	
	jsr	INIT_CONTROL	;	Set initial speed & steering	
MAIN	jsr jsr jsr jsr bra	DIRECTION SPEED MODE ARBITRATOR MAIN	;;;;;	Check steering for collision Check speed for collision Check if in Radio control mode Carry out steering and throttle Repeat cycle forever	
	sei swi		; ;	disable interrupts Terminate program execution	
<pre>************************************</pre>	ARBITRA Analyze robot s None. None. None. None.	ATOR es all informations should go.	***	**************************************	* * * * * * * *
ARBITRATOR	psha		;	Save register	~ ~
	ldaa beq	RADIO_MODE AUTONOMOUS	;	Check if in radio control mode	
RADIO_CONTROL	ldd std ldd std bra	PWMRSERVO PWMSERVO PWMRESC PWMESC RARBITRATOR	;;;;	Get steering from radio receiver Set steering accordingly Get throttle from radio receiver Set throttle accordingly	
AUTONOMOUS	ldaa	COLLISION_DIR			
GOSTRAIGHT	cmpa bne jsr bra	#CS GOHARDLEFT Straight GOSTOP	;	Set steering to straight	
GOHARDLEFT	cmpa bne jsr bra	#CHL GOHARDRIGHT HardLeft GOSTOP	;	Set steering to hard left	
GOHARDRIGHT	cmpa bne jsr	#CHR GOMEDLEFT HardRight	;	Set steering to hard right	
GOMEDLEFT	cmpa bne jsr	GOSIOP #CML GOMEDRIGHT MedLeft	;	Set steering to medium left	
GOMEDRIGHT	bra cmpa bne jsr	GOSTOP #CMR GOSOFTLEFT MedRight	;	Set steering to medium right	

GOSOFTLEFT GOSOFTRIGHT	bra cmpa bne jsr bra cmpa bne jsr	GOSIOP #CSL GOSOFTRIGHT SoftLeft GOSTOP #CSR GOSTOP SoftRight	; Set steering to soft left ; Set steering to soft right	
GOSTOP	ldaa cmpa bne jsr bra	COLLISION_SPEED #CSTOP GOSLOWR Stop RARBITRATOR		
GOSLOWR	cmpa bne jsr bra	#CSLOWR GOSLOWF SlowRev RARBITRATOR		
GOSLOWF	cmpa bne jsr bra	#CSLOWF GOMEDF SlowFrw RARBITRATOR		
GOMEDF	cmpa bne jsr bra	#CMEDF GOMAXF MedFrw RARBITRATOR		
GOMAXF	cmpa bne jsr	#CMAXF RARBITRATOR MaxFrw		
RARBITRATOR	pula rts		; restore register ; return from subroutine	
<pre>* Subroutine: * Function: * * * Input: * Output: * Destroys:</pre>	MODE Determi: signals is a va None. None.	nes whether we ar coming out of th lid pulse modulat	e in radio control mode by analyzing le radio receiver and checking if it ed signal.	* * * * * * *
* Calle:	None.			*
* Calls:	None. None. *******	* * * * * * * * * * * * * * * * * * *	*****	 * *
* Calls: ***********************************	None. None. ******** psha	* * * * * * * * * * * * * * * * * * *	**************************************	 * *
* Calls: ***********************************	None. None. ******** psha pshb	****	**************************************	 * *
* Calls: ***********************************	None. None. ******** psha pshb clr ldd	**************************************	<pre>************************************</pre>	* *
* Calls: ***********************************	None. None. ******** psha pshb clr ldd cpd bbi	**************************************	<pre>************************************</pre>	* *
* Calls: ***********************************	None. None. ******** psha pshb clr ldd cpd bhi cpd	**************************************	<pre>************************************</pre>	* *
* Calls: ***********************************	None. None. ******** psha pshb clr ldd cpd bhi cpd bhi	**************************************	<pre>************************************</pre>	* *
* Calls: ***********************************	None. None. ******** psha pshb clr ldd cpd bhi cpd bhi cpd blo ldd	RADIO_MODE PWMRSERVO #4050 RMODE #1950 RMODE PWMRESC	<pre>************************************</pre>	* *
* Calls: ***********************************	None. None. ******** psha pshb clr ldd cpd bhi cpd blo ldd cpd	**************************************	<pre>************************************</pre>	* * *
* Calls: ***********************************	None. None. ******** psha pshb clr ldd cpd bhi cpd blo ldd cpd bhi	<pre>************************************</pre>	<pre>************************************</pre>	. * *
* Calls: ***********************************	None. None. ******** psha pshb clr ldd cpd bhi cpd blo ldd cpd bhi cpd bhi cpd bhi	<pre>************************************</pre>	<pre>************************************</pre>	* * *
* Calls: ***********************************	None. None. ******** psha pshb clr ldd cpd bhi cpd blo ldd cpd bhi cpd blo ldd cpd bhi cpd bhi cpd bhi cpd bhi	<pre>RADIO_MODE PWMRSERVO #4050 RMODE #1950 RMODE PWMRESC #4050 RMODE #1950 RMODE #1950 RMODE #1950 RMODE TOTAL</pre>	<pre> /************************************</pre>	* * *
* Calls: ***********************************	None. None. ******** psha pshb clr ldd cpd bhi cpd blo ldd cpd bhi cpd bhi cpd blo ldd cpd	RADIO_MODE PWMRSERVO #4050 RMODE #1950 RMODE PWMRESC #4050 RMODE #1950 RMODE #1950 RMODE TOTAL #39900	<pre>************************************</pre>	* *
* Calls: ***********************************	None. None. ******** psha pshb clr ldd cpd bhi cpd blo ldd cpd bhi cpd blo ldd cpd bhi c cpd cpd bhi c cpd bhi c cpd bhi c cpd c cpd c cpd c cpd c c cpd c c cpd c c cpd c c cpd c c c c	<pre>************************************</pre>	<pre>************************************</pre>	* *
* Calls: ***********************************	None. None. ******** psha pshb clr ldd cpd bhi cpd blo ldd cpd bhi cpd blo ldd cpd bhi	RADIO_MODE PWMRSERVO #4050 RMODE #1950 RMODE PWMRESC #4050 RMODE #1950 RMODE #1950 RMODE TOTAL #39900 RMODE #40100 RMODE	<pre>************************************</pre>	* *
* Calls: ***********************************	None. None. ******** psha pshb clr ldd cpd bhi cpd blo ldd cpd bhi cpd blo ldd cpd blo ldd cpd bhi ldd a	<pre>RADIO_MODE PWMRSERVO #4050 RMODE #1950 RMODE PWMRESC #4050 RMODE #1950 RMODE TOTAL #39900 RMODE #40100 RMODE #1</pre>	<pre>; Save register ; Save register ; Disable radio mode ; Is the servo pulse to wide? ; Is the servo pulse to narrow? ;Is the ESC pulse to wide? ;Is the ESC pulse to narrow? ;Is the total period around 20ms? ; Enable radio mode</pre>	* *
* Calls: ***********************************	None. None. ******** psha pshb clr ldd cpd bhi cpd blo ldd cpd bhi cpd blo ldd cpd bhi cpd bhi ldd cpd bhi slo ldd cpd bhi slo ldd cpd bhi slo ldd cpd bhi slo lo ldd cpd bhi slo lo ldd cpd bhi cpd bhi slo lo lo ldd cpd bhi slo lo lo ldd cpd bhi cpd cpd cpd cpd cpd cpd cpd cpd cpd cpd	<pre>RADIO_MODE PWMRSERVO #4050 RMODE #1950 RMODE #1950 RMODE #1950 RMODE #1950 RMODE #1950 RMODE #19900 RMODE #40100 RMODE #1 RADIO_MODE</pre>	<pre>; Save register ; Save register ; Disable radio mode ; Is the servo pulse to wide? ; Is the servo pulse to narrow? ;Is the ESC pulse to wide? ;Is the ESC pulse to narrow? ;Is the total period around 20ms? ; Enable radio mode</pre>	* *
* Calls: ***********************************	None. None. ******** psha pshb clr ldd cpd bhi cpd blo ldd cpd bhi cpd blo ldd cpd bhi ldaa staa pulb	RADIO_MODE PWMRSERVO #4050 RMODE #1950 RMODE PWMRESC #4050 RMODE #1950 RMODE #1950 RMODE #1950 RMODE #1950 RMODE #40100 RMODE #40100 RMODE #1 RADIO_MODE	<pre>; Save register ; Save register ; Disable radio mode ; Is the servo pulse to wide? ; Is the servo pulse to narrow? ;Is the ESC pulse to wide? ;Is the ESC pulse to narrow? ;Is the total period around 20ms? ; Enable radio mode ; restore register</pre>	* *
* Calls: *********************** MODE RMODE	None. None. ******** psha pshb clr ldd cpd bhi cpd blo ldd cpd bhi cpd blo ldd cpd blo ldd cpd bhi lda a staa pulb pula	RADIO_MODE PWMRSERVO #4050 RMODE #1950 RMODE PWMRESC #4050 RMODE #1950 RMODE #1950 RMODE TOTAL #39900 RMODE #40100 RMODE #1 RADIO_MODE	<pre>; Save register ; Save register ; Disable radio mode ; Is the servo pulse to wide? ; Is the servo pulse to narrow? ;Is the ESC pulse to wide? ;Is the ESC pulse to narrow? ;Is the total period around 20ms? ; Enable radio mode ; restore register ; restore register ; Potture from subrouting</pre>	* *

* * * * * * * * * * * * * * *	* * * * * * * * *	* * * * * * * * * * * * * * * * *	***	* * * * * * * * * * * * * * * * * * * *
* SUBROUTINE: * FUNCTION: *	DIRECTIO Read IR left, or but sets	DN sensors and dete right. This fun a direction var	ern not	* nine whether robot should go straight,* tion does not actually move the wheels* able for later analyzing. *
* INPUT: * OUTPUT: * DESTROYS:	None. None. None			* *
* CALLS:	None.			*
****	******	*****	***	* * * * * * * * * * * * * * * * * * * *
DIRECTION	psha pshb		; ;	Save register Save register
STRAIGHT_TEST	ldaa cmpa bhs ldaa cmpa bhs ldaa cmpa bhs ldaa staa bra	ADR1 #90 HARD_LEFT_TEST ADR2 #90 HARD_LEFT_TEST ADR3 #90 HARD_LEFT_TEST #CS COLLISION_DIR RDIRECTION	;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;	Read Left IR Sensor Check if path is clear to left Test failed so do next test Read Center IR Sensor Check if path is clear in front Test failed so do next test Read Right IR Sensor Check if path is clear to right Test failed so do next test Test passed and therefore path is clear Set Collision direction to straight Done with collision direction
HARD_LEFT_TEST	ldaa suba cmpa bls ldaa staa bra	ADR3 #L_DIFF ADR1 HARD_RIGHT_TEST #CHL COLLISION_DIR RDIRECTION	;;;;;;;;	Read Right IR Sensor Large difference in left & right sensor; Read Left IR Sensor Test failed so do next test There is a close object on right side Set Collision direction to hard left Done with collision direction
HARD_RIGHT_TEST	ldaa suba cmpa bls ldaa staa bra	ADR1 #L_DIFF ADR3 MED_LEFT_TEST #CHR COLLISION_DIR RDIRECTION	;;;;;;;;	Read Left IR Sensor Large difference in left & right sensors Read Right IR Sensor Test failed so do next test There is a close object on left side Set Collision direction to hard right Done with collision direction
MED_LEFT_TEST	ldaa suba cmpa bls ldaa staa bra	ADR3 #M_DIFF ADR1 MED_RIGHT_TEST #CML COLLISION_DIR RDIRECTION	;;;;;;;;	Read Right IR Sensor Medium diff in left & right sensors Read Left IR Sensor Test failed so do next test Somewhat close object on right side Set Collision direction to medium left Done with collision direction
MED_RIGHT_TEST	ldaa suba cmpa bls ldaa staa bra	ADR1 #M_DIFF ADR3 SOFT_LEFT_TEST #CMR COLLISION_DIR RDIRECTION	;;;;;;;	Read Left IR Sensor Medium diff in left & right sensors Read Right IR Sensor Test failed so do next test Somewhat close object on left side Set Collision direction to medium right Done with collision direction
SOFT_LEFT_TEST	ldaa suba cmpa bls ldaa staa bra	ADR3 #S_DIFF ADR1 SOFT_RIGHT_TEST #CSL COLLISION_DIR RDIRECTION	;;;;;;;;	Read Right IR Sensor Small difference in left & right sensor; Read Left IR Sensor Test failed so do next test There is an object on right side Set Collision direction to soft left Done with collision direction
SOFT_RIGHT_TEST	ldaa	ADR1	;	Read Left IR Sensor

	suba	#S_DIFF	; Small difference in left & right sensors
	cmpa	ADR3	; Read Right IR Sensor
	bls	RDIRECTION	; Test failed so do next test
	ldaa	#CSR	; There is an object on left side
	staa	COLLISION_DIR	; Set Collision direction to soft right
	bra	RDIRECTION	; Done with collision direction
RDIRECTION	pulb		; Restore register
	pula		; Restore register
	rts		; return from subroutine
* * * * * * * * * * * * * * * * *	* * * * * * * * *	* * * * * * * * * * * * * * * * * * *	*************
* SUBROUTINE:	SPEED		*
* FUNCTION:	Read IR	sensors and dete	rmine what speed the robot should be *
*	going.	Also determines	whether it should be going forward or *
*	reverse	. This function	does not actually move the wheels but *
*	sets a d	direction variabl	e for later analyzing. *
* INPUT:	None.		*
* OUTPUT:	None.		*
* DESTROYS:	None.		*
* CALLS:	None.		*
****	* * * * * * * * *	* * * * * * * * * * * * * * * * * *	* * * * * * * * * * * * * * * * * * * *
SPEED	psha		; Save register
	pshb		; Save register
	ldaa	COLLISION SDEED	
	ruaa	#CGLOWD	
	bne	STOD TEST	
	inc	DEVTIME	
	ldaa	PEVTIME	
	cmpa	#SFFFF	
	bea	STOP TEST	
	jmp	RSPEED	
פייטי עניט	ldaa	1 סת ג	: Pead Left IP Sensor
SIOP_IESI	Iuaa	H107	· Check if path is blocked to left
	blo	HIZ/ MAY EDW TECT ·	Togt foiled as do next togt
	ldaa	MAA_FRW_IESI /	· Pood Contor IP Songer
	Iuaa	ADRZ	, Read Center IR Sensor
	cllipa	HIZ/ MAX EDW TECT ·	Test foiled as do next test
	ldaa	MAA_FRW_IESI /	: Pead Pight IP Senger
	ruaa	H127	: Check if path is blocked to right
	blo	MAY FRW TEST :	Test failed so do next test
	ldaa	ADR4	: Read Rear IR Sensor
	cmpa	#127	: Check if noth is blocked to the rear
	blo	MAX FRW TEST :	Test failed so do next test
	ldaa	#CSTOP	: Test passed and so path is totally blocked
	staa	COLLISION SPEED	; Set speed to stop
	bra	RSPEED	; Done with collision speed
אאע דסע יידימיי	ldaa	1 סת ג	: Pead Left IP Sensor
MAA_FRW_IESI	Iuaa	H00	· Chock if path is glear to left
	Cilipa bbi	HYU Med Edw Test	. Togt failed go do next togt
	ldaa	MED_FRW_IESI	· Pead Contor IP Songer
	Iuaa	H00	· Chock if noth is abor in front
	bhi	אדט המא הבכת אבט המא	: Test failed so do next test
	ldaa	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	: Pead Pight TP Senger
	cmpa	±90	; Check if path is clear to right
	bhi	דיי אבט גאמ הבקע	: Test failed so do next test
	ldaa	HCMAXE	: Test passed and therefore path is clear
	-uaa gtaa	UUTTATI	; Set speed to maximum
	bra	RSPEED	; Done with collision speed
Μፑክ ፑቦਯ ጥፑሮጥ	ldaa	ADP 1	: Pead Left ID Sensor
LED_LKM_1E91	Tuad	H105	, reau mert in pensor
	bhi	עריד אפא הבכה מו"טיי אפא הבכה	: Test failed so do next test
	ldaa	2000 T.Y.M TEST	, rest ratted so do mext test . Read Center ID Censor
	Tuad	AURZ	, rear center it bensol

	cmpa bhi ldaa cmpa bhi ldaa staa bra	#105 SLOW_FRW_TEST ADR3 #105 SLOW_FRW_TEST #CMEDF COLLISION_SPEED RSPEED	<pre>; Check if path is somewhat clear in fr ; Test failed so do next test ; Read Right IR Sensor ; Check if path is somewhat clear to r: ; Test failed so do next test ; Test passed and so path is somewhat of ; Set speed to medium ; Done with collision speed</pre>	ront ight clear
SLOW_FRW_TEST	ldaa cmpa bhi ldaa cmpa bhi ldaa cmpa bhi ldaa staa bra	ADR1 #129 SLOW_REV_TEST ADR2 #129 SLOW_REV_TEST ADR3 #129 SLOW_REV_TEST #CSLOWF COLLISION_SPEED RSPEED	<pre>; Read Left IR Sensor ; Check if path is somewhat blocked to ; Test failed so do next test ; Read Center IR Sensor ; Check if path is somewhat blocked in ; Test failed so do next test ; Read Right IR Sensor ; Check if path is somewhat blocked to ; Test failed so do next test ; Test passed and so path is somewhat b ; Set speed to slow ; Done with collision speed</pre>	left front right blocked
SLOW_REV_TEST	ldaa cmpa bls ldaa staa bra	ADR4 #127 SLOW_REV_1 #CSTOP COLLISION_SPEED RSPEED	; Read Rear IR Sensor ; Check if path is clear to rear ; Test failed so do next test	
SLOW_REV_1	ldaa staa clr bra	#CMEDR COLLISION_SPEED REVTIME RSPEED	; Test passed and therefore path is clea ; Set speed to reverse ; Done with collision speed	ar
RSPEED	pulb pula rts		; Restore register ; Restore register ; Return from subroutine	
<pre>************************************</pre>	******** INIT_CO Set thr None. None. None.	**************************************	**************************************	* * * * * *
**************************************	<pre>interview is it is</pre>	***************** Stop Straight	<pre>************************************</pre>	* *
<pre>************************************</pre>	******** Straigh Center None. None. None. None.	**************************************	These wheels are controlled by a servo	* * • * • * * * *
Straight	psha pshb ldd std pulb pula rts	#STRAIGHT_PWM PWMSERVO	<pre>; Save register ; Save register ; Store pulse width high time ; Restore register ; Restore register ; return from subroutine</pre>	* *
* SUBROUTINE:	HardLef	t		*

* FUNCTION: Turn wheels all the way to the left. These wheels are are controlled by a servo. * INPUT: None. * OUTPUT: None. * DESTROYS: None. * CALLS: None. ; Save register HardLeft psha pshb ; Save register ldd #HARDLEFT_PWM ; Store pulse width high time std PWMSERVO ; Restore register pulb pula ; Restore register rts ; return from subroutine * SUBROUTINE: HardRight * FUNCTION: Turn wheels all the way to the right. These wheels are are controlled by a servo. * INPUT: None. * OUTPUT: None. * DESTROYS: None. * CALLS: None. HardRight ; Save register psha pshb ; Save register ldd #HARDRIGHT_PWM std PWMSERVO ; Store pulse width high time pulb ; Restore register pula ; Restore register ; return from subroutine rts * SUBROUTINE: MedLeft * FUNCTION: Turn wheels half way to the left. These wheels are controlled * by a servo. * INPUT: None. * OUTPUT: None. * DESTROYS: None. * CALLS: None. MedLeft psha ; Save register pshb ; Save register ldd #MEDLEFT_PWM PWMSERVO std ; Store pulse width high time pulb ; Restore register pula ; Restore register ; return from subroutine rts * SUBROUTINE: MedRight * FUNCTION: Turn wheels half way to the right. These wheels are controlled * by a servo. * INPUT: None. * OUTPUT: None. * DESTROYS: None. * CALLS: None. ***** MedRight psha ; Save register ; Save register pshb ldd #MEDRIGHT PWM std PWMSERVO ; Store pulse width high time pulb ; Restore register pula ; Restore register rts ; return from subroutine

* SUBROUTINE: SoftLeft * FUNCTION: Turn wheels a little to the left. These wheels are controlled by a servo. * INPUT: None. * OUTPUT: None. * DESTROYS: None. * CALLS: None. ******* SoftLeft psha ; Save register pshb ; Save register ldd #SOFTLEFT PWM ; Store pulse width high time std PWMSERVO pulb ; Restore register pula ; Restore register rts ; return from subroutine * SUBROUTINE: SoftRight * FUNCTION: Turn wheels a little to the right. These wheels are controlled * by a servo. * INPUT: None. * OUTPUT: None. * DESTROYS: None. * * CALLS: None. ******* SoftRight ; Save register psha ; Save register pshb ldd #SOFTRIGHT_PWM std PWMSERVO ; Store pulse width high time pulb ; Restore register pula ; Restore register rts ; return from subroutine * SUBROUTINE: Stop * FUNCTION: Set throttle speed to off. Throttle is controlled by an electronic speed control (ESC) which expects a pulse modulated * signal. * INPUT: None. * OUTPUT: None. * DESTROYS: None. * CALLS: None. ; Save register psha Stop pshb ; Save register ldd #STOP_PWM std PWMESC ; Store pulse width high time pulb ; Restore register ; Restore register pula ; return from subroutine rts * SUBROUTINE: MaxFrw * Set throttle to maximum forward speed. Throttle is controlled * FUNCTION: by an electronic speed control (ESC) which expects a pulse width modulated signal. * INPUT: None. * OUTPUT: None. * DESTROYS: None. * CALLS: None. MaxFrw psha ; Save register pshb ; Save register ldd #MAXFRW_PWM std PWMESC ; Store pulse width high time ; Restore register pulb ; Restore register pula

; return from subroutine

* * * * * * * * * * * * * * * *	* * * * * * * * * * * * * * * * * * * *	* * * * * * * * * * * * * * * * * * * *
<pre>* SUBROUTINE: * FUNCTION: * * * INPUT: * OUTPUT: * DESTROYS: * CALLS: ***********************************</pre>	MaxRev Set throttle to maximu by an electronic speed width modulated signal None. None. None. ************************************	<pre>* * * * * * * * * * * * * * * * * * *</pre>
	pula	; Restore register
	rts	; return from subroutine
**************************************	<pre>************************************</pre>	<pre>************************************</pre>
MedFrw	psha	; Save register
	pshb ldd #MEDFRW_PWM std PWMESC pulb pula rts	; Save register ; Store pulse width high time ; Restore register ; Restore register ; return from subroutine
* * * * * * * * * * * * * * * *	* * * * * * * * * * * * * * * * * * * *	*****
* SUBROUTINE: * FUNCTION: * * * INPUT: * OUTPUT: * DESTROYS: * CALLS:	MedRev Set throttle to medium by an electronic speed width modulated signal None. None. None.	* reverse speed. Throttle is controlled * control (ESC) which expects a pulse * * *
*****	* * * * * * * * * * * * * * * * * * * *	***************************************
MEAKEY	psna pshb ldd #MEDREV_PWM std PWMESC pulb pula rts	<pre>; save register ; Save register ; Store pulse width high time ; Restore register ; Restore register ; return from subroutine</pre>
* * * * * * * * * * * * * * * *	* * * * * * * * * * * * * * * * * * * *	*****
<pre>* SUBROUTINE: * FUNCTION: * * * INPUT: * OUTPUT: * DESTROYS: * CALLS: ***********************************</pre>	SlowFrw Set throttle to slow f an electronic speed co modulated signal. None. None. None. None.	* * * * * * * * * * * * * * * * * * *
SlowFrw	psha	; Save register

rts

	pshb		; Save register	
	ldd	#SLOWFRW_PWM		
	std	PWMESC	; Store pulse width high time	
	pulb		; Restore register	
	pula		; Restore register	
	rts		; return from subroutine	
* * * * * * * * * * * * * * * *	* * * * * * * *	* * * * * * * * * * * * * * * * *	******	*
* SUBROUTINE:	SlowRe	v		*
* FUNCTION:	Set th	rottle to slow re	everse speed. Throttle is controlled by	*
*	an ele	ctronic speed con	ntrol (ESC) which expects a pulse width	*
*	modula	ted signal.		*
* INPUT:	None.			*
* OUTPUT:	None.			*
* DESTROYS:	None.			*
* CALLS:	None.			*
*************	*******	* * * * * * * * * * * * * * * * * *	***************************************	*
SIOWREV	psna		, Save register	
	psno	HOLOWDEN DWM	, save register	
	atd	#SHOWICEV_FWM	: Store pulse width high time	
	pulb	FWHESC	; Bestore register	
	pulb		: Restore register	
	rts		; return from subroutine	
	200			
* * * * * * * * * * * * * * * *	*******	* * * * * * * * * * * * * * * * * *	* * * * * * * * * * * * * * * * * * * *	*
* SUBROUTINE:	OC2ISR	~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~	ad signal on autout someone 2 (sin 20)	*
* FUNCTION ·	Thia	ignal is used to	ed signal on output compare 2 (pin 28).	*
* דאדורואד	Nono	ignal is used to	control the throttle of the robot.	*
* OUTDUT:	None			*
* DESTROYS:	None			*
* CALLS:	None.			*
				÷
* * * * * * * * * * * * * * * *	* * * * * * * *	* * * * * * * * * * * * * * * * * *	* * * * * * * * * * * * * * * * * * * *	^
OC2ISR	psha	* * * * * * * * * * * * * * * * * * *	**************************************	î
OC2ISR	psha pshb	* * * * * * * * * * * * * * * * * * * *	**************************************	Ŷ
**************************************	psha pshb ldaa	#BIT6	**************************************	^
**************************************	psha pshb ldaa staa	#BIT6 TFLG1	**************************************	^
**************************************	psha pshb ldaa staa ldaa	#BIT6 TFLG1 TCTL1	**************************************	~
**************************************	psha pshb ldaa staa ldaa ANDA	#BIT6 TFLG1 TCTL1 #BIT6 LECTLO	**************************************	~
*******************************	******** psha pshb ldaa staa ldaa ANDA BEQ	#BIT6 TFLG1 TCTL1 #BIT6 LASTLOW	**************************************	~
C2ISR	********* psha pshb ldaa staa ldaa ANDA BEQ BRA ldaa	#BIT6 TFLG1 TCTL1 #BIT6 LASTLOW LASTHI	**************************************	~
**************************************	******** psha pshb ldaa staa ldaa ANDA BEQ BRA ldaa OPA	<pre>#BIT6 #BIT6 TFLG1 TCTL1 #BIT6 LASTLOW LASTHI TCTL1 #BIT6</pre>	**************************************	~
C2ISR LASTLOW	******** psha pshb ldaa staa ldaa ANDA BEQ BRA ldaa ORA	<pre>#BIT6 TFLG1 TCTL1 #BIT6 LASTLOW LASTHI TCTL1 #BIT6 TCTL1 #BIT6 TCTL1</pre>	**************************************	~
C2ISR LASTLOW	******** psha pshb ldaa staa ldaa ANDA BEQ BRA ldaa ORA staa ldd	<pre>#BIT6 TFLG1 TCTL1 #BIT6 LASTLOW LASTHI TCTL1 #BIT6 TCTL1 #BIT6 TCTL1 #BFFFOD</pre>	**************************************	~
C2ISR LASTLOW	******** psha pshb ldaa staa ldaa ANDA BEQ BRA ldaa ORA staa ldd SUBD	<pre>#BIT6 TFLG1 TCTL1 #BIT6 LASTLOW LASTHI TCTL1 #BIT6 TCTL1 #BIT6 TCTL1 #PERIOD PWMESC</pre>	**************************************	~
C2ISR LASTLOW	******** psha pshb ldaa staa ldaa ANDA BEQ BRA ldaa ORA staa ldd SUBD ADDD	<pre>#BIT6 TFLG1 TCTL1 #BIT6 LASTLOW LASTHI TCTL1 #BIT6 TCTL1 #BIT6 TCTL1 #PERIOD PWMESC TCC2</pre>	**************************************	~
C2ISR LASTLOW	******** psha pshb ldaa staa ldaa ANDA BEQ BRA ldaa ORA staa ldd SUBD ADDD std	<pre>#BIT6 TFLG1 TCTL1 #BIT6 LASTLOW LASTHI TCTL1 #BIT6 TCTL1 #BIT6 TCTL1 #PERIOD PWMESC TOC2 TOC2 TCC2</pre>	**************************************	~
C2ISR LASTLOW	******** psha pshb ldaa staa ldaa ANDA BEQ BRA ldaa ORA staa ldd SUBD ADDD std BRA	<pre>#BIT6 TFLG1 TCTL1 #BIT6 LASTLOW LASTHI TCTL1 #BIT6 TCTL1 #BIT6 TCTL1 #PERIOD PWMESC TOC2 TOC2 RTOC2 RTOC2</pre>	**************************************	~
LASTLOW	******** psha pshb ldaa staa ldaa ANDA BEQ BRA ldaa ORA staa ldd SUBD ADDD std BRA ldaa	<pre>#BIT6 TFLG1 TCTL1 #BIT6 LASTLOW LASTHI TCTL1 #BIT6 TCTL1 #BIT6 TCTL1 #PERIOD PWMESC TOC2 TOC2 RTOC2 TCC1</pre>	**************************************	~
C2ISR LASTLOW	******** psha pshb ldaa staa ldaa ANDA BEQ BRA ldaa ORA staa ldd SUBD ADDD std BRA ldaa ANDA	<pre>#BIT6 TFLG1 TCTL1 #BIT6 LASTLOW LASTHI TCTL1 #BIT6 TCTL1 #PERIOD PWMESC TOC2 TOC2 RTOC2 RTOC2 TCTL1 #IBIT6</pre>	**************************************	~
LASTLOW	******** psha pshb ldaa staa ldaa ANDA BEQ BRA ldaa ORA staa ldd SUBD ADDD std BRA ldaa ANDA staa	<pre>#BIT6 TFLG1 TCTL1 #BIT6 LASTLOW LASTHI TCTL1 #BIT6 TCTL1 #BIT6 TCTL1 #PERIOD PWMESC TOC2 TOC2 RTOC2 TCC2 RTOC2 TCTL1 #IBIT6 TCTL1</pre>	<pre>************************************</pre>	~
LASTLOW	******** psha pshb ldaa staa ldaa ANDA BEQ BRA ldaa ORA staa ldd SUBD ADDD std BRA ldaa ANDA staa ldaa ANDA	<pre>#BIT6 TFLG1 TCTL1 #BIT6 LASTLOW LASTHI TCTL1 #BIT6 TCTL1 #BIT6 TCTL1 #PERIOD PWMESC TOC2 TOC2 RTOC2 TCC2 TCC1 #IBIT6 TCTL1 #IBIT6 TCTL1 TC2</pre>	<pre>************************************</pre>	~
C2ISR LASTLOW	******** psha pshb ldaa staa ldaa ANDA BEQ BRA ldaa ORA staa ldd SUBD ADDD std BRA ldaa ANDA staa ldd aLdaa ANDA	<pre>#BIT6 TFLG1 TCTL1 #BIT6 LASTLOW LASTHI TCTL1 #BIT6 TCTL1 #PERIOD PWMESC TOC2 TOC2 RTOC2 TCTL1 #IBIT6 TCTL1 #IBIT6 TCTL1 TC2 PWMESC</pre>	<pre>************************************</pre>	~
C2ISR LASTLOW	******** psha pshb ldaa staa ldaa ANDA BEQ BRA ldaa ORA staa ldd SUBD ADDD std BRA ldaa ANDA staa ldd atda Staa staa ldd std	<pre>#BIT6 TFLG1 TCTL1 #BIT6 LASTLOW LASTHI TCTL1 #BIT6 TCTL1 #PERIOD PWMESC TOC2 TOC2 RTOC2 TCTL1 #IBIT6 TCTL1 #IBIT6 TCTL1 TOC2 PWMESC TOC2 PWMESC TOC2</pre>	<pre>************************************</pre>	~
C2ISR LASTLOW LASTHI RTOC2	******** psha pshb ldaa staa ldaa ANDA BEQ BRA ldaa ORA staa ldd SUBD ADDD std BRA ldaa ANDA staa ldd astaa ldd std pulb	<pre>#BIT6 TFLG1 TCTL1 #BIT6 LASTLOW LASTHI TCTL1 #BIT6 TCTL1 #PERIOD PWMESC TOC2 TOC2 RTOC2 TCTL1 #IBIT6 TCTL1 #IBIT6 TCTL1 TOC2 PWMESC TOC2 PWMESC TOC2</pre>	<pre>; Save register ; Save register ; Clear OC2 Interrupt Flag ; Restore register</pre>	~
C2ISR C2ISR LASTLOW LASTHI RTOC2	******** psha pshb ldaa staa ldaa ANDA BEQ BRA ldaa ORA staa ldd SUBD ADDD std BRA ldaa ANDA staa ldd ADDD std pulb pula	<pre>#BIT6 TFLG1 TCTL1 #BIT6 LASTLOW LASTHI TCTL1 #BIT6 TCTL1 #PERIOD PWMESC TOC2 TOC2 RTOC2 TCTL1 #IBIT6 TCTL1 #IBIT6 TCTL1 TOC2 PWMESC TOC2 PWMESC TOC2</pre>	<pre>; Save register ; Save register ; Clear OC2 Interrupt Flag ; Clear oc2 Interrupt Flag ; Restore register ; Restore register</pre>	~
C2ISR LASTLOW LASTHI RTOC2	******** psha pshb ldaa staa ldaa ANDA BEQ BRA ldaa ORA staa ldd SUBD ADDD std BRA ldaa ANDA staa ldd ADDD std pulb pula RTI	<pre>#BIT6 TFLG1 TCTL1 #BIT6 LASTLOW LASTHI TCTL1 #BIT6 TCTL1 #PERIOD PWMESC TOC2 TCC2 RTOC2 TCTL1 #IBIT6 TCTL1 #IBIT6 TCTL1 TOC2 PWMESC TOC2 PWMESC TOC2</pre>	<pre>; Save register ; Save register ; Clear OC2 Interrupt Flag ; Clear oc2 Interrupt Flag ; Restore register ; Restore register ; Restore register ; Restore register ; Return from OC2 ISR</pre>	~
<pre>************************************</pre>	******** psha pshb ldaa staa ldaa ANDA BEQ BRA ldaa ORA staa ldd SUBD ADDD std BRA ldaa ANDA staa ldd ADDD std pulb pula RTI	<pre>#BIT6 TFLG1 TCTL1 #BIT6 LASTLOW LASTHI TCTL1 #BIT6 TCTL1 #PERIOD PWMESC TOC2 TCC2 TCC2 RTOC2 TCTL1 #IBIT6 TCTL1 #IBIT6 TCTL1 TOC2 PWMESC TOC2</pre>	<pre>; Save register ; Save register ; Clear OC2 Interrupt Flag ; Restore register ; Restore register ; Restore register ; Restore register ; Return from OC2 ISR</pre>	*
**************************************	******** psha pshb ldaa staa ldaa ANDA BEQ BRA ldaa ORA staa ldd SUBD ADDD std BRA ldaa ANDA staa ldd ADDD std pulb pula RTI ********	<pre>#BIT6 TFLG1 TCTL1 #BIT6 LASTLOW LASTHI TCTL1 #BIT6 TCTL1 #PERIOD PWMESC TOC2 TCC2 RTOC2 TCTL1 #IBIT6 TCTL1 #IBIT6 TCTL1 TOC2 PWMESC TOC2</pre>	<pre>; Save register ; Save register ; Clear OC2 Interrupt Flag ; Restore register ; Restore register ; Restore register ; Return from OC2 ISR</pre>	* *
<pre>************************************</pre>	******** psha pshb ldaa staa ldaa ANDA BEQ BRA ldaa ORA staa ldd SUBD ADDD std BRA ldaa ANDA staa ldd ADDD std pulb pula RTI ******** OC3ISR Produc	<pre>#BIT6 TFLG1 TCTL1 #BIT6 LASTLOW LASTHI TCTL1 #BIT6 TCTL1 #PERIOD PWMESC TOC2 TCC2 TCC2 RTOC2 TCTL1 #IBIT6 TCTL1 #IBIT6 TCTL1 TOC2 PWMESC TOC2</pre>	<pre>; Save register ; Save register ; Clear OC2 Interrupt Flag ; Restore register ; Restore register ; Restore register ; Return from OC2 ISR ed signal on output compare 3 (pin 29).</pre>	× * *
<pre>************************************</pre>	******** psha pshb ldaa staa ldaa ANDA BEQ BRA ldaa ORA staa ldd SUBD ADDD std BRA ldaa ANDA staa ldd ADDD std pulb pula RTI ******** OC3ISR Produc This s	<pre>#BIT6 TFLG1 TCTL1 #BIT6 LASTLOW LASTHI TCTL1 #BIT6 TCTL1 #PERIOD PWMESC TOC2 TCC2 TCC2 RTOC2 TCC2 RTOC2 TCTL1 #IBIT6 TCTL1 #IBIT6 TCTL1 TOC2 PWMESC TOC2 ************************************</pre>	<pre>; Save register ; Save register ; Clear OC2 Interrupt Flag ; Restore register ; Restore register ; Restore register ; Return from OC2 ISR ed signal on output compare 3 (pin 29). control the direction of the robot.</pre>	<pre></pre>
<pre>************************************</pre>	******** psha pshb ldaa staa ldaa ANDA BEQ BRA ldaa ORA staa ldd SUBD ADDD std BRA ldaa ANDA staa ldd ADDD std pulb pula RTI ******** OC3ISR Produc This s None.	<pre>#BIT6 TFLG1 TCTL1 #BIT6 LASTLOW LASTHI TCTL1 #BIT6 TCTL1 #PERIOD PWMESC TOC2 TCC2 TCC2 RTOC2 TCTL1 #IBIT6 TCTL1 #IBIT6 TCTL1 TOC2 PWMESC TOC2 PWMESC TOC2</pre>	<pre>; Save register ; Save register ; Clear OC2 Interrupt Flag ; Restore register ; Restore register ; Restore register ; Return from OC2 ISR ed signal on output compare 3 (pin 29). control the direction of the robot.</pre>	~ * * * *
<pre>************************************</pre>	******** psha pshb ldaa staa ldaa ANDA BEQ BRA ldaa ORA staa ldd SUBD ADDD std BRA ldaa ANDA staa ldd ADDD std pulb pula RTI ******** CC3ISR Produc This s None. None.	<pre>#BIT6 TFLG1 TCTL1 #BIT6 LASTLOW LASTHI TCTL1 #BIT6 TCTL1 #PERIOD PWMESC TOC2 TCC2 TCC2 RTOC2 TCTL1 #IBIT6 TCTL1 #IBIT6 TCTL1 TOC2 PWMESC TOC2 PWMESC TOC2</pre>	<pre>; Save register ; Save register ; Clear OC2 Interrupt Flag ; Restore register ; Restore register ; Restore register ; Return from OC2 ISR ed signal on output compare 3 (pin 29). control the direction of the robot.</pre>	~ * * * * *

* CALLS:	None.		*
* * * * * * * * * * * * * * * *	* * * * * * * * *	* * * * * * * * * * * * * * * * * * *	* * * * * * * * * * * * * * * * * * * *
OC3ISR	psha	;	Save register
	pshb	;	Save register
	ldaa	#BIT5 ;	Clear OC3 Interrupt Flag
	staa	TFLG1	
	ldaa	TCTL1	
	ANDA	#BIT4	
	BEQ	LSTLOW	
	BRÃ	LSTHI	
LSTLOW	ldaa	TCTL1	
	ORA	#BIT4	
	staa	TCTL1	
	ldd	#PERIOD	
	SUBD	PWMSERVO	
		TOC3	
	std	TOC3	
	BRA	RTOC3	
Т.СТНТ	ldaa	тстт.1	
	ANDA		
	ldd		
	ADDD	PWMSERVO	
58002	sta	1003	De staar of star
RTOC3	pulb	;	Restore register
	pula	;	Restore register
	R'I'I	;	Return from OC3 ISR
* * * * * * * * * * * * * * * *	*******	* * * * * * * * * * * * * * * * * * * *	****
* SUBROUTINE:	OC4ISR		*
* FUNCTION:	Used as	a timer to detect	when the radio receiver signal goes *
*	dead. :	Input capture will	still be waiting for a rising edge *
*	pulse so	o it will not trig	ger. This routine will set the radio *
*	throttle	e and steering set	tings to zero to relinquish control *
*	to the a	autonomous routine	·S. *
* INPUT:	None.		*
* OUTPUT:	None.		*
* DESTROYS:	None.		*
* CALLS:	None.		*
* * * * * * * * * * * * * * *	* * * * * * * * *	* * * * * * * * * * * * * * * * * * *	* * * * * * * * * * * * * * * * * * * *
OC4ISR	psha	;	Save register
	pshb	;	Save register
	ldaa	#BIT4 ;	Clear OC4 Interrupt Flag
	staa	TFLG1	
	ldd	#0 ;	Radio receiver went dead so,
	std	PWMRSERVO ;	clear radio steering and
	std	PWMRESC ;	throttle pulses.
RTOC4	pulb	;	Restore register
	pula	;	Restore register
	RTI	;	Return from OC4 ISR
* * * * * * * * * * * * * * * *	* * * * * * * * *	* * * * * * * * * * * * * * * * * *	* * * * * * * * * * * * * * * * * * * *
* SUBROUTINE:	IC2ISR		*
* FUNCTION:	Read the	e pulse modulated	signal on input capture 2 (pin 33). *
*	This is	the receiver sign	al for the throttle of the robot. *
* INPUT:	None.		*
* OUTPOUT:	None		*
* DESTROVS:	None		*
* CALLS:	None		*
**************************************	********	* * * * * * * * * * * * * * * * * *	* * * * * * * * * * * * * * * * * * * *
TC2TSR	psha	:	Save register
	pshb	,	Save register
	ldaa	, #BITT1 ,	Clear IC2 Interrupt Elag
	ataa	TFIG1	CICUL ICA INCCLIUPE FIAS
	ldaa		
	anda		
	hea		Wag gapturing on high to low
	NEY	MUDIITOII I	Was capturing on night-to-iow

WASHIGH	bra ldd std subd std ldaa anda ora staa ldd std bra ldd std std ldd std ldd std	WASLOW TIC2 L_TIC2 LAST_TIC2 PWMRESC TCTL2 #%11110111 #%00000100 TCTL2 TCNT TOC4 RTIC2 TIC2 LAST_TIC2 TOTAL TIC2 LAST_TIC2	<pre>; Was capturing on low-to-high ; Calculate pulse width ; Store throttle pulse width ; Set IC2 to capture on low-to-high ; Low-to-high capture ; Store time to check if radio ; signal went dead</pre>	
	ldaa anda ora	TCTL2 #%11111011 #%00001000	; Set IC2 to capture on high-to-low	
	staa	TCTL2	; High-to-low capture	
RTIC2	pulb pula RTI		; Restore register ; Restore register ; Return from IC2 ISR	
* * * * * * * * * * * * * * * *	* * * * * * * *	* * * * * * * * * * * * * * * * * *	******	***
* SUBROUTINE: * FUNCTION: * * INPUT: * OUTPUT: * DESTROYS: * CALLS:	IC3ISR Read th This is None. None. None.	e pulse modulated the receiver sig	signal on input capture 3 (pin 34). nal for the steering of the robot.	* * * * * *
TC3TSR	nsha	* * * * * * * * * * * * * * * * * * * *	; Save register	* * *
ICSISK	psha pshb ldaa staa ldaa anda beq bra	#BIT0 TFLG1 TCTL2 #BIT0 WSHIGH WSLOW	<pre>; Save register ; Save register ; Clear IC3 Interrupt Flag ; Was capturing on high-to-low ; Was capturing on low-to-high</pre>	
WSHIGH	ldd subd std ldaa	TIC3 LAST_TIC3 PWMRSERVO TCTL2	; Calculate pulse width ; Store steering pulse width ; Set IC3 to capture on low-to-high	
	anda ora staa bra	#%1111101 #%00000001 TCTL2 RTIC2	; Low-to-high capture	
WSLOW	ldd std ldaa anda ora staa	TIC3 LAST_TIC3 TCTL2 #%1111110 #%00000010 TCTL2	; Set IC3 to capture on high-to-low	
RTIC3	pulb pula RTI	10122	; Restore register ; Restore register ; Return from IC3 ISR	
* * * * * * * * * * * * * * * *	* * * * * * * *	* * * * * * * * * * * * * * * * * *	*****	* * *

*	the subroutine exits.				*
* INPUT:	None.				*
* OUTPUT:	Register A = input from SCI				*
* DESTROYS:	A regist	ter.			*
* CALLS:	None.				*
* * * * * * * * * * * * * * * *	* * * * * * * * *	* * * * * * * * * * * * * * * * *	* * *	* * * * * * * * * * * * * * * * * * * *	*
IN_CHAR	ldaa anda beq ldaa rts	SCSR #%00100000 IN_CHAR SCDR	;;;;;	check status reg. check if receive buffer full wait until data present data -> A register return from subroutine	
* * * * * * * * * * * * * * * *	* * * * * * * * *	* * * * * * * * * * * * * * * * * *	* * *	*****	*
* Subroutine:	OUT_CHAP	R			*
* Function:	Outputs the character in register A to the screen				
*	once the transmission data register is empty.				
* Input:	Data to be transmitted in register A.				*
* Output:	Transmitted data.				*
* Destroys:	None.				*
* Calls:	None.				*
*****	* * * * * * * * *	* * * * * * * * * * * * * * * * * *	* *	* * * * * * * * * * * * * * * * * * * *	*
OUT_CHAR L_OUT_CHAR	pshb ldab andb beq staa pulb rts	SCSR #\$80 L_OUT_CHAR SCDR	;;;;;;;;	Save register Check status register. Check if trans. buffer empty. Wait until empty. Output character. restore register Return from subroutine	
	end		;	End of program	