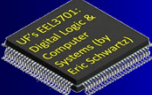**EEL3701** — Immediately Prior to the Lab Quiz

- Below has been shown to prevent problems that have happened to a few students in the past. It wouldn't hurt and could save some aggravation.
  - > Verify that you have your UF ID, passport, or driver's license (for the Honorlock scan).
  - > Clear your cache in Chrome.
    - – Reinstall the Chrome Honorlock Extension.
    - – Close then reopen Chrome.
  - > Verify that you have the files that you need (and are allowed) available from your desktop.
  - > Reboot your computer.
- Be sure that there is enough light for your face to be clear during the quiz (for Honorlock). If not, find a different area.

University of Florida, EEL 3701 – File **Honorlock & Lab Quiz**
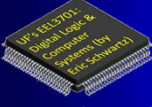© Dr Eric M. Schwartz

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EEL3701 — Immediately Prior to the Lab Quiz

- Close **all programs/applications** other than Chrome.
- Close **all Chrome tabs** other than Canvas.
- Turn off any potential pop-ups that could occur during this assignment.

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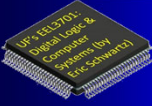© Dr Eric M. Schwartz

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EEL3701 — Bring to the all Labs

- Bring the following to all labs:
  - > **Your PC, with web cam & speakers**
  - > **Your <u>entire</u> 3701 kit (all parts)**
    - – Your 74'xxx devices
    - – *DE10-Lite*, USB cables, and if necessary, a USB expander
  - > **Your DAD**
  - > Two completely blank pieces of scratch paper, with nothing written or printed on either side
- Your breadboard, when the exam begins:
  - > Can have the following on your breadboard:
    - – Vcc and GND connected to the PLD PCB and the power rails
    - – Note: **During** the quiz, don't forget to **connect your DAD's ground**, **DE10-Lite's** ground, and **breadboard round**, thus making a necessary common ground.
  - > Can**NOT** have the following on your breadboard :
    - – Any other components or wires
    - – Switch circuits or components
    - – LED circuits or components
    - – 74'xxx ICs

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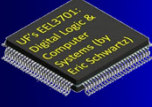© Dr Eric M. Schwartz

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EEL3701 — Honorlock ID Scan Extras

- After your Honorlock ID scan, show the following to your camera.
  - > Both sides of your scratch paper.
  - > Your breadboard (containing nothing not allowed).
- You will scan and submit images of this paper with CamScanner or equivalent as specified later in this document.

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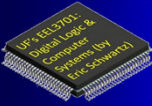© Dr Eric M. Schwartz

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EEL3701 **Paper Submission at End of Lab Quiz**

- **<u>DURING</u>** the lab quiz
  - >Use of MS-Word (or similar) and email is **<u>NOT</u>** allowed.
  - >No physical phone connection to your computer is allowed.
  - >No printer/scanner is allowed.
- Be sure to **view** the file on your phone **before** submission to assure that it is **complete and clear**.
- Bring **2** completely blank pieces of paper and pens/pencils.

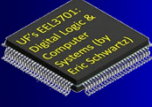University of Florida, EEL 3701 – File **Honorlock & Lab Quiz**
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EEL3701 **Submissions During Lab Quiz**

- In the *Lab X Quiz – Generic* assignment, submit the following as specified:
  - >Submit an image of your design (bdf)
  - >Submit an image of your simulation output
  - >Archive your design and submit your **Quartus archive** (qar) file

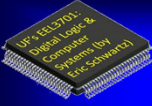University of Florida, EEL 3701 – File **Honorlock & Lab Quiz**
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EEL3701 — Scans at End of Lab Quiz

- Only after you are completed the rest of the exam, upload **a single pdf** file (use CamScanner or equivalent) containing each of the following to the *Lab X Quiz – Phone* assignment:
  1. An image of your scratch paper with each of the following.
     a) The equation.
     b) The truth table and voltage table.
     c) The circuit diagram with labeled pin numbers (for 74'XX components).
     d) The switch and LED legends.
  2. An image of your breadboard with circuit.

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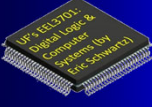© Dr Eric M. Schwartz

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EEL3701 — Integrity

- After touching your phone or archiving Quartus, **no more writing/work** is allowed!
- Once you start scanning your paper/breadboard or start archiving you Quartus project, you are **no longer allowed** to make any changes (including the following):
  > Your circuit design on your breadboard.
  > Your Quartus design, simulation, or PLD program.
  > Your scratch paper.
- No more work is allowed, other than the required submissions.

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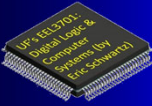© Dr Eric M. Schwartz

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EEL3701 Honorlock During Lab Quiz

- In Honorlock, you must share your computer screen, video, and audio with the proctoring PI at all times. Do **NOT** close Honorlock until after your demo.
- If you are finished before the time is up, let the PI know, and when the PI has time, they will let you demo.
- If you are not finished until the deadline, the assignment will automatically close when time is up. It is therefore **critical** that you upload your required images and files before time is up!
  - > If you do not, then you will **earn a zero** on the lab quiz.

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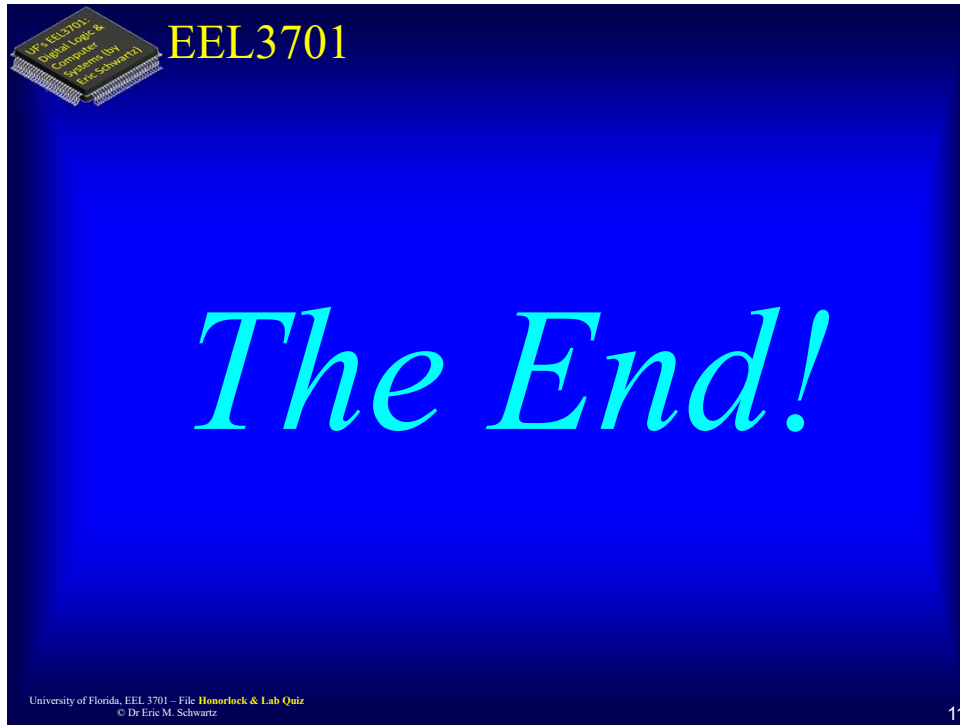© Dr Eric M. Schwartz

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EEL3701 Demo for Lab Quiz

- You will demo your lab quiz to your PI. Do **NOT** modify your circuit after your submissions.
- When you are ready (after you have submitted everything required), let your PI know and then wait for your PI to be ready for your demo.
  - > Be prepared to run your design, as instructed, with your DAD connected as specified.
  - > Do not close Honorlock until your PI says that they are ready for you to demo.

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EEL3701

The End!

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