University of Florida
Department of Electrical & Computer Engineering

Exam Rules

You must read, understand, and abide by the following:

- *All electronics, except your single computer, mouse, webcam, and cell phone must be turned off and put away. Only a single monitor can be used.*
  - *You can**not** use any programs on your computer, other than Zoom, Chrome, and Honorlock.*
  - *You can**not** use any notes, homework, labs, books, or calculators.*
- *Your cell phone should be in reach but turned so that its face is down; do not touch your phone until you are done. As soon as you touch it, no more work is allowed on either the computer or paper and scanning of your paper should begin.*
- *Besides the above, **only** the following materials are allowed: **seven** for exam 1 (and **ten** for the final exam) completely blank pieces of paper and writing utensils.*
  - *You must show each of the blank pages to the camera (front and back) at the start of your exam.*
  - *You will scan and submit these papers with Adobe Scan or equivalent, as specified below.*
  - *Clearly write the problem number for each of the problems on your scratch pages.*
  - *Only after you have completed the rest of the exam (or when 5 minutes are remaining), do the following:*
    - *Use your cell phone to scan your utilized scratch pages. Pages not scanned will not be graded!*
    - *View your scan to verify that all pages are clear and included.*
    - *Using your cell phone, upload your scan as specified to the **Exam Phone** assignment.*
- *Before your exam begins, connect to Zoom with your camera off. During your exam, you can ask a questions after "raising your hand."*
  - *Do not attempt to chat with your classmates.*
  - *If you have a question, "raise your hand" by selecting **React** and then **Raise Hand**.*
  - *Dr. Schwartz or a PI will initiate a **Private Chat**. Be patient, since there may be several students before you.*
  - *When Dr. Schwartz or a PI starts the chat, immediately specify the question number, part, paragraph number, and sentence number.*
- *If you lose your Honorlock connection, **immediately** try to reconnect. If you still have problems, use Honorlock's "Live Chat" (on the right) to get it corrected.*
  - *While doing a Live Chat, immediately raise your hand to tell a PI or Dr. Schwartz what is goingon.*
- *Read each question **carefully** and **follow the instructions**.*
- *The point values for problems may be changed at Dr. Schwartz's discretion.*
- *For circuit diagrams, use **labels** to identify physical connections instead of drawing lines (wires).*
- *For anything undefined, if necessary, state a reasonable assumption. (But ask Dr. Schwartz or a PI first.)*
- *Failure to follow the below rules will result in **NO** partial credit.*
  - *The **base** (radix) of all numbers must be indicated with a **subscript** or **prefix**.*
  - *Truth tables, voltage tables, and timing simulations must be in **counting** order.*
  - *Label the inputs and outputs of each circuit with activation-levels.*
  - *For each mixed-logic circuit diagram, label inputs of **each** gate with the appropriate logic equations.*
  - *For K-maps, label **each** grouping with the appropriate equation.*
  - *Labels inside of parts must be provided whenever it can be confusing, e.g., they **MUST** be specified for MUXes and Decoders, etc., but not for NANDs and ORs, etc.*
  - *For each circuit design, equations must **not** be used as replacements for circuit elements.*
  - *Boolean expression answers must be in **lexical order**, (i.e., /A before A, B before B, & D_3 before D_2).*

PLEDGE: *On my honor as a University of Florida student, I certify that (1) I have read, understood, and will abide by the above, (2) I will neither give nor receive any unauthorized aid during this examination, and (3) if I sense (i.e., see, hear, etc.) anyone else give or receive unauthorized aid during this examination (or learn of it afterwards), I will report it immediately to Dr. Schwartz.*