*Note: Late HW is **not** accepted!*

1. Complete parts I, II, III, and V of the *Quartus Tutorial*. Submit the design and simulation as a Quartus archive file through Canvas (called **Quartus_Tutorial.qar**). Include **screenshots** of the design and simulations in your homework document file.

2. Make a truth table and voltage table of the equation given in the tutorial. Verify that the voltage table matches the voltage simulations from Quartus.