*Note: Late HW is **not** accepted!*

1. Use a Quartus simulation to show that an S-R latch can be made using two NAND gates.

2. Use a Quartus simulation to show that an S-R latch can be made using two NOR gates.

3. Derive the characteristic equations and excitation tables for each type of flip-flop. See Table 12-9 in your Roth textbook.

4. Roth textbook problems:
    5th, 6th and 7th edition: 11.1, 11.3, 11.8, 11. 9
    5th edition: 11.21
    6th and 7th edition: 11.26