

Errors in Programming Reference Guide (Bible)

- In the SCSR register description on page 57, SCSR is often called SCSR1 for no good reason.
- The TOI bit description in the TMSK2 register on page 63 is wrong. The “0=” line *should* read: “0 = TOF interrupts **disabled**.”
- The MODF bit description in the SPSR register on page 60 is wrong. The sentence *should* read: “Clear this flag cleared by reading SPSR with MODF set, then write to SPCR.”
- The Boolean expressions of the following functions are incorrect and should be fixed as shown below where **M** stands for the appropriate memory address, **(M)** stands for the data at address **M**, **mm** for the 8-bit mask, and **/X** = “not X.” The Bible is general inconsistent in the use of **M** and **(M)**, as is much of Motorola’s (and others) documentation, i.e., sometimes **M** stands for data at memory location **M** as well as address **M**!

Mnemonic	Boolean Expression	Notes
BSET	$(M) + mm \rightarrow (M)$	OK in bible, but included for completeness Ex: 0000 1111 + 1000 0001 = 1000 1111
BCLR	$(M) \bullet /mm \rightarrow (M)$	Ex: 0000 1111 \bullet / (1000 0001) = 0000 1110
BRSET	$? / (M) \bullet mm = 0$	BRSET PORTD, X \$80 * ; Wait for PE7 set
BRCLR	$? (M) \bullet mm = 0$	BRCLR SCSR, X \$20 * ; Wait for RDRF (bit5)