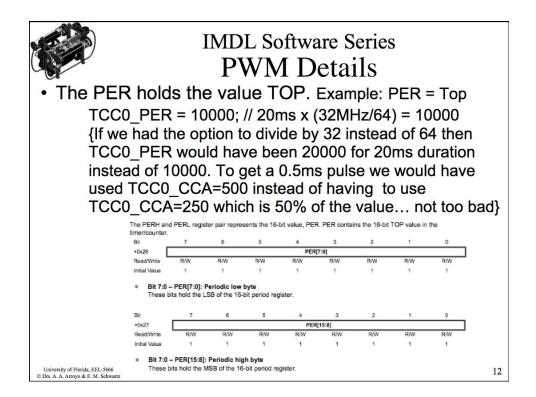
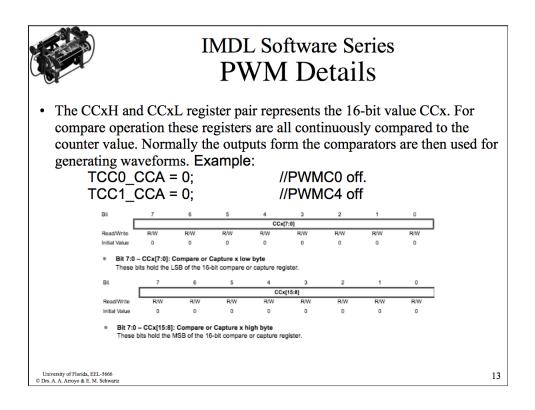
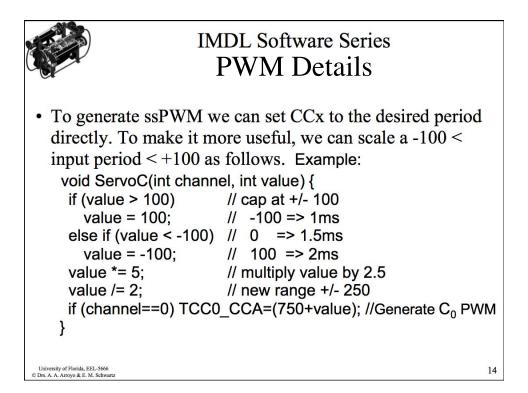


| • The PWN | /I clock is | PV | L Softw VM I in the C | Deta | ils | | Example | e: |
|-----------|---|--|--|---|--------------|------------------|--------------|----|
| TCC | 0 CTRL | A = 0x05; | //se | t TCC | | K to (| CI K/64 | |
| 100 | CTRLA - Control re | | 1100 | | 0_01 | | | |
| | CIRLA - Control n | egister A | | | | | | |
| | Bit 7 | 6 | 5 4 | 3 | 2 | 1 | 0 | |
| | +0x00 | | | | | EL[3:0] | | |
| | Read/Write R Initial Value 0 | R | R R 0 0 | R/W | R/W | R/W | R/W | |
| | These bits sele CLKSEL=0001 | EL[3:0]: Clock Select ct the clock source for th must be set to ensure a | | 5 | | en the hi-res e | extension is | |
| | These bits sele | ct the clock source for th must be set to ensure a | | 5 | | en the hi-res e | extension is | |
| | These bits sele CLKSEL=0001 enabled. | ct the clock source for th must be set to ensure a | correct output from | the waveform | | en the hi-res e | extension is | |
| | These bits sele CLKSEL=0001 enabled. Table 14-3. Clock se | ct the clock source for th must be set to ensure a lect options. | correct output from | the waveform | generator wh | en the hi-res e | extension is | |
| | These bits sele CLKSEL=0001 enabled. Table 14-3. Clock se CLKSEL[3:0] | ct the clock source for th must be set to ensure a lect options. Group configurat | correct output from | the waveform | generator wh | en the hi-res e | extension is | |
| | These bits sele CLKSEL=0001 enabled. Table 14-3. Clock se CLKSEL[3:0] 0000 | ct the clock source for th must be set to ensure a lect options. Group configurat OFF | correct output from tion Description None (i.e, 1 | the waveform m timer/counter in Clk | generator wh | en the hi-res e | extension is | |
| | These bits sele CLKSEL=0001 enabled. Table 14-3. Clock se CLKSEL[3:0] 0000 0001 | ct the clock source for th must be set to ensure a lect options. Group configurat OFF DIV1 | tion Description None (i.e, 1) Prescaler: | the waveform imer/counter in Clk Clk/2 | generator wh | en the hi-res e | extension is | |
| | These bits sele CLKSEL=0001 enabled. Table 14-3. Clock se CLKSEL[3:0] 0000 0001 0001 | ct the clock source for th must be set to ensure a lect options. Group configurat OFF DIV1 DIV2 | tion Descriptio None (i.e, 1 Prescaler: Prescaler: | the waveform timer/counter in Clk Clk/2 Clk/4 | generator wh | en the hi-res o | extension is | |
| | These bits sele CLKSEL=0001 enabled. Table 14-3. Clock se CLKSEL[3:0] 0000 0001 0001 0010 0010 | ct the clock source for th must be set to ensure a lect options. Group configurat OFF DIV1 DIV2 DIV4 | tion Descriptio None (i.e, Prescaler: Prescaler: | the waveform - timer/counter in Clk Clk/2 Clk/4 Clk/8 | generator wh | en the hi-res e | extension is | |
| | These bits sele CLKSEL=0001 enabled. Table 14-3. Clock se CLKSEL[3:0] 0000 0001 0001 0010 0011 0010 | et the clock source for th must be set to ensure a lect options. Group configurat OFF DIV1 DIV2 DIV2 DIV4 DIV8 | tion Descriptio None (i.e, 1 Prescaler: Pres | the waveform - timer/counter in Clk Clk/2 Clk/4 Clk/8 Clk/64 | generator wh | en the hi-res d | extension is | |
| | These bits sele CLKSEL=0001 enabled. Table 14-3. Clock se CLKSEL[3:0] 0000 0001 0001 0010 0011 0100 0101 | et the clock source for th must be set to ensure a lect options. Group configurat OFF DIV1 DIV2 DIV4 DIV8 DIV64 | tion Description Ion None (i.e, Prescaler: P | the waveform - timer/counter in Clk Clk/2 Clk/4 Clk/6 Clk/64 Clk/256 | generator wh | en the hi-res of | extension is | |

| A THE | | | IN | ADI | L Soft | war | e S | eries | |
|----------|---------------|--|----------------|---------------|----------------------------|---------------|--------------|---|-----|
| T E | | | | נוס | | | + | 1 | |
| | | | | ΡV | VM I | De | lai | 15 | |
| PWM W | G is se | lected | in the | CTRI | B rea E | yam | nle: | | |
| // Enabl | | | | | | | | | |
| | | | | | | | | | |
| | | | | CCx | and 0 fr | om C | CCX to | о Тор. | |
| TCC0 | CTR | LB = 0 | (F3; | | | | | | |
| | Bit | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| | +0x01 | CCDEN | CCCEN | CCBEN | CCAEN | - | | WGMODE[2:0] | |
| | Read/Write | R/W | R/W | R/W | R/W | R | R/W | R/W | R/W |
| | Initial Value | 0 Bit 7:4 – CCxEl | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | | for the correspo When input cap channel. | | | e CCxEN bits enable | e the capture | operation fo | or the corresponding | сс |
| | ۰ | | t the waveform | generation m | node, and control th | | | the counter, TOP val cording to Table 14-4 | |
| | | No waveform ge | | | | | | des, the result from th | |
| | | waveform gene this. The port pi | | | | orresponding | g CCxEN bit | has been set to enal | ble |
| | | | limer waveform | | | | | | |
| | | WGMODE | 2:0] Group | configuration | Mode of operation | Тор | Update | OVFIF/Event | |
| | | 000 | NORMA | | Normal | PER | TOP | TOP | |
| | | 001 | FRQ | | Frequency | CCA | TOP | TOP | |
| | | 010 | | | Reserved | - | - | - | |
| | | 0.11 | SINGLES | SLOPE | Single-slope PWM | PER | BOTTOM | BOTTOM | |
| | | 011 | | | | | | | |
| | | 100 | | | Reserved | - | - | - | |
| | | | DSTOP | | Reserved Dual-slope PWM | - PER | BOTTOM | TOP TOP and BOTTOM | |

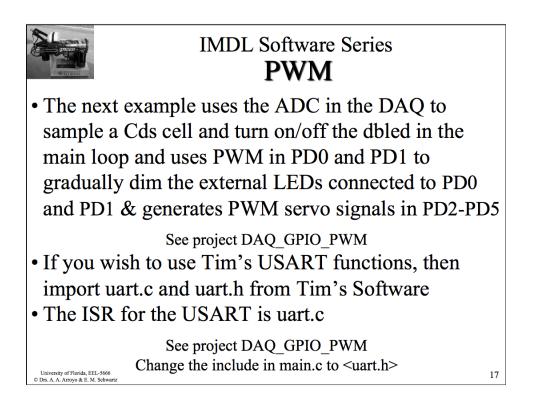


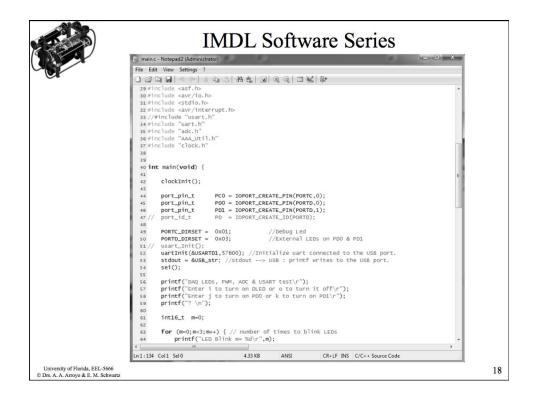


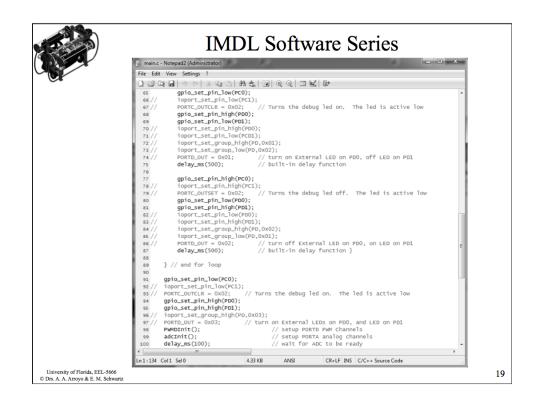


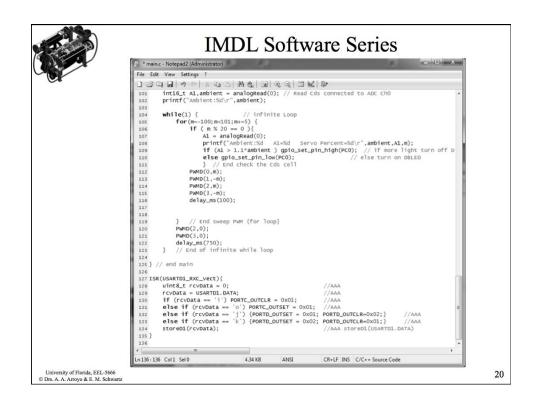
| AAA* | _Util.c - Notepad2 (Administrator) | n n | _ — × | [|
|----------|------------------------------------|------------------------------|---|---|
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| | Da 🖌 🤊 🗠 🕺 🖬 🖪 🕯 | 1 eb. 🗊 🔍 🔍 📰 📈 | (R+ | |
| | d PWMCInit(void) { | | ialize PWM Channels on PortC | |
| 29 | $TCC0_CTRLA = 0x05;$ | // set TCC0_CLK t | 0 CLK/64 | |
| 30 | TCC0 CTRLB = $0x33$; | | . Set to Single Slope PWM | |
| 31 | - , | // OCnX = 1 from | Bottom to CCx and 0 from CCx to | |
| 32 | TCC0_PER = 10000; | // 20ms / (1/(32M | Hz/64)) = 10000. PER = Top | |
| 33 | $TCC1_CTRLA = 0x05;$ | // set TCC1_CLK t | 0 CLK/64 | |
| 34 | $TCC1_CTRLB = 0x33;$ | // Enable OC A an | d B. Set to Single Slope PWM | |
| 35 | | // OCNX = 1 from | Bottom to CCx and 0 from CCx to | |
| 36 | TCC1_PER = 10000; | // 20ms / (1/(32M | HZ/64)) = 10000. PER = Top | |
| 37 | PORTC_DIRSET = 0x33; | <pre>// set PORTC5:4,1</pre> | :0 to output | |
| 38 // | PORTC_DIR = 0x33; | <pre>// set PORTC5:4,1</pre> | :0 to output | |
| 39 | TCC0_CCA = 0; | // PWMC0 off | | |
| 40 | тсс0_ссв = 0; | // PWMC1 off | | |
| 41 // | $TCC0_CCC = 0;$ | // PWMC2 off | | |
| 42 // | $TCC0_CCD = 0;$ | // PWMC3 off | | |
| 43 | $TCC1_CCA = 0;$ | // PWMC4 off | | |
| 44 | TCC1_CCB = 0; | // PWMC5 off | | |
| 45 } | // End PWMCINIT | | | |
| 46 | | | | |
| 47 voi | | value) { // | / PWMC: Generate PWM on PortC pin | |
| 48 | int ledval, serval; | | | |
| 49 | if (value > 100) value = | | 'cap at +/- 100 | |
| 50 | else if (value < -100) | /alue = -100; // | -100 => 1ms 0 => 1.5ms 100 => | |
| 51 | ledval=value; | | | |
| 52 | serval=value; | | | |
| 53 | serval *= 5; | | ' multiply value by 5 {avoid floa | |
| 5.4 | ledval *= 50; | | new range O <range<10000 for="" lei<="" td=""><td></td></range<10000> | |
| 55 | | | Generate CO PWM for LEDs. | |
| 56 | | | Generate C1 PWM for LEDs. | |
| 57 // | | | Generate CO PWM for Servos. | |
| 58 // | | | Generate C1 PWM for Servos. | |
| 59// | | | Generate C2 PWM for Servos. | |
| | | | Generate C3 PWM for Servos. | |
| 61 | | | Generate C4 PWM for Servos. | |
| 62 | | = (750 + serval); // | Generate C5 PWM for Servos. | |
| 63 } | // end PWMC | | * | |
| 4 | | | | |
| EL-5666 | | | | |

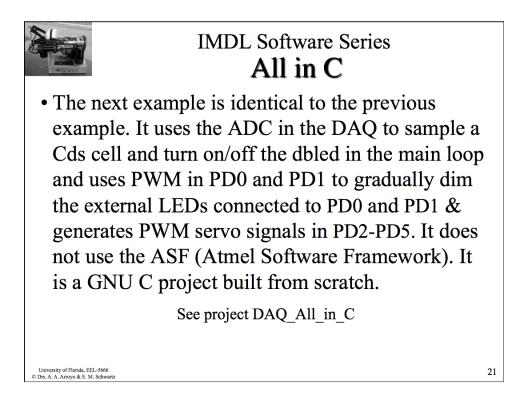
| AAA | _Util.c - Notepad2 (| Administrator) | 1.1 | | | | | |
|----------|--|--------------------------------|------------|-----------|-----------|-------------|------------------------|--|
| File E | dit View Setting | s ? | | | | | | |
| 06 | | X 03 13 4 | a ab | Q Q [| | | | |
| 28 VC | 28 void PWMDInit(void) { // PWMDINIT: Initialize PWM Channels on PortD | | | | | | | |
| 29 | TCD0_CTRLA | = 0x05; | 11 50 | et TCDO C | LK to CLK | /64 | | |
| 30 | TCD0_CTRLB | | | | | | to single S | |
| 31 | | | | | | | nd 0 from ccx | |
| 32 | TCD0_PER | = 10000; | | | | | PER = Top | |
| 33 | TCD1_CTRLA | | | | LK to CLK | | | |
| 34 | TCD1_CTRLB | = 0x33; | // Et | nable oc | A and B. | Set to Si | ngle Slope PW | |
| 35 | | | // 00 | nx = 1 f | rom Botto | m to CCx a | nd 0 from CCx | |
| 36 | TCD1_PER | = 10000; | // 20 | Dms / (1/ | (32MHz/64 |)) = 10000 | PER = Top | |
| 37 | PORTD_DIRSE | T = 0x3F; | // se | et PORTD | pins <5:0 | > to output | e entre contrator E | |
| 38 // | PORTCD_DIR | = 0x3F; | // se | et PORTD | pins <5:0 | > to output | £ | |
| 39 | TCD0_CCA | = 0; | // P\ | MD0 off | | | | |
| 40 | TCD0_CCB | = 0; | // P\ | MD1 off | | | | |
| 41 | TCD0_CCC | = 0; | // P\ | MD2 off | | | | |
| 42 | | = 0; | | WD3 off | | | | |
| 43 | | = 0; | | WD4 off | | | | |
| 44 | TCD1_CCB | = 0; | // P\ | MD5 off | | | | |
| 45 } | // End PWMD | INIT | | | | | | |
| 46 | | | | | | | | |
| | | channel, int | value) { | | // PWME |): Generate | PWM on Portc | |
| 48 | int ledval, | | | | | | | |
| 49 | | 100) value = | | 1002 | | at +/- 100 | 1000 | |
| 50 | | alue < -100) | value = -1 | 100; | // -100 | % => 0.5ms | 0% => 1.5ms | |
| 51 | ledval=valu | | | | | | | |
| 52 | serval=valu | | | | 11 | | h | |
| 53 | serval *= 5 | | | | | | by 5 {avoid | |
| 5.4 | ledval *= 5 | | (5000 | 7 | | | nge<10000 fo | |
| 55 | | | | | | | | |
| 56 | | ==1) TCD0_CCB | | | | | | |
| 57 // | | | | | | | | |
| 58 // | | ==1) TCD0_CCB | | | | | | |
| 59 | | ==2) TCD0_CCC | | | | | | |
| 60 | | ==3) TCD0_CCD | | | | | | |
| 61 62 | | ==4) TCD1_CCA ==5) TCD1_CCB | | | | | | |
| 62 | // end PWMD | | - (750 + | serval); | // Gene | Tate 05 PW | TOT Servos. | |

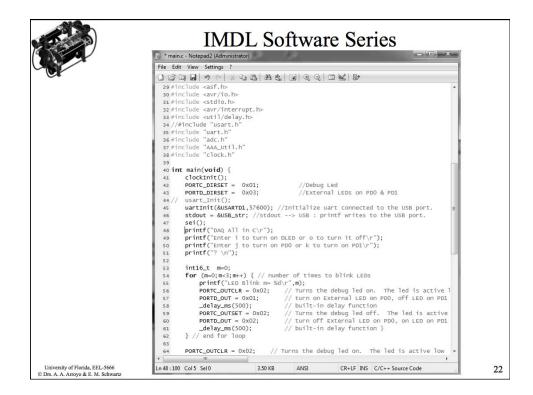


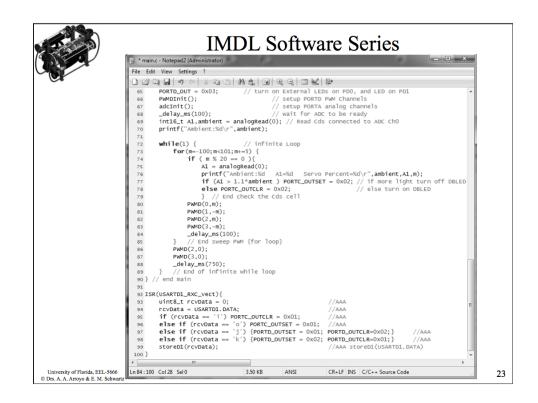


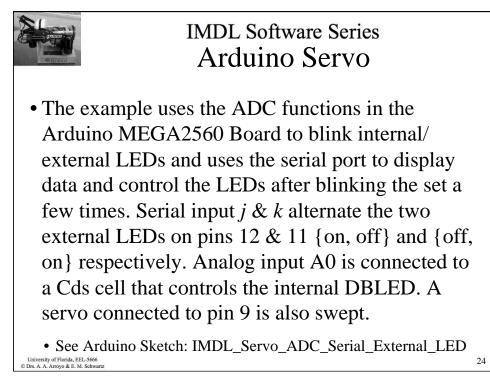












| IMDL_Servo_ADC_Serial_Exter | rnal_LED.c - Notepad2 (Administrator) | - • × |
|---------------------------------|--|-------|
| File Edit View Settings ? | | |
| | δ 12 13 14 th 12 0 0 0 10 12 10 10 | |
| 1/* | | |
| | 4: Use of Digital Output Pins | |
| | rnal LEDs connected to Pins 11 & 12 | |
| | d the internal LED on Pin 13 on/off. | |
| | ach state of 1/2 sec. Use serial input | |
| | EDs. {i,o} for internal LED {j,k} for | |
| | ead Cds cell on Analog AO and control | |
| 9 Arroyo September | . Use pin 9 to sweep a servo. | E |
| 10 AFFOYO September | AV LVAT | |
| 11 */ | | |
| 12 | | |
| 13 #include <servo.h></servo.h> | | |
| | create servo object to control a servo | |
| 15 | | |
| | // External LED connected to digital pin 11 | |
| | <pre>// External LED connected to digital pin 12 // Internal LED connected to digital pin 13</pre> | |
| 18 Inc Incced = 15; | // Incernal LED connected to digital pin 15 | |
| 20 void setup() { | | |
| | OUTPUT); // sets digital pin 12 as output | |
| <pre>22 pinMode(extLED2,</pre> | OUTPUT); // sets digital pin 12 as output | |
| | OUTPUT); // sets digital pin 13 as output | |
| 24 Serial.begin(5760 | | |
| 25 myservo.attach(9) |); // attaches servo to pin 9 | |
| 26 } | | |
| | count=0, j=0, a1, ambient; | |
| 29 void loop() { | Junceo, jeo, az, amorenc, | |
| 30 if (count < 1) { | | |
| | ("Arduino MEGA2560 Example"); | |
| 32 Serial.println(| ("Cds Cell connected on Analog AO"); | |
| 33 Serial.println(| ("Control the internal LED on pin 13"); | |
| 34 ambient = analo | | |
| 35 Serial.print("A | | |
| 36 Serial.println(| (ambient); | - |
| < III | | • |
| Ln1:87 Col1 Sel0 | 3.33 KB ANSI LF INS C/C++ Source Code | |

| | IDL_Servo_ADC_Serial_External_LED.c - Notepad2 (Administrator) | × |
|------|---|---|
| File | Edit View Settings ? | |
| | 20.00 × 00 × 00 × 00 × 00 × 00 | |
| 37 | for (j=0; j<5; j++){ | |
| 38 | serial.print("i= "); | |
| 39 | Serial.println(j); | |
| 40 | digitalWrite(13, HIGH); // set the internal LED on | |
| 41 | digitalwrite(12, LOW); // set the external LED1 off | |
| 42 | digitalwrite(11, HIGH); // set the external LED2 on | |
| 43 | delay(500); // wait for a 1/2 second | |
| 44 | digitalwrite(13, LOW); // set the internal LED off | |
| 45 | digitalwrite(12, HIGH); // set the external LED on | |
| 46 | <pre>digitalwrite(11, LOW); // set the external LED off delaw(roo); // write for a 1/2 accord</pre> | |
| 47 | delay(500); // wait for a 1/2 second | |
| 48. | } digitalWrite(13, HIGH); // set the internal LED on | |
| 50 | digitalWrite(12, HIGH); // set the external LED1 on | |
| 51 | digitalWrite(11, HIGH): // set the external LED2 on | |
| 52 | i graam rectar, maay, 27 bet die enternar tebe an | |
| 53 | count=1: | |
| 5.4 | <pre>Serial.print("Ambient: ");</pre> | |
| 55 | <pre>Serial.print(ambient);</pre> | |
| 56 | Serial.print(" A1: "); | |
| 5.7: | a1 = analogRead(A0); | |
| 58 | <pre>if (a1 > 1.1*ambient) digitalWrite(13, LOW); // turn off DBLED</pre> | |
| 59 | else digitalwrite(13, HIGH); // else turn on DBLED | |
| 60 | Serial.println(a1); | |
| 61 | | |
| | // Sweep Servo | |
| 63 | <pre>for(j = 0; j < 180; j += 1) { // goes from 0 degrees to 180 degrees myservo.write(j); // tell servo to go to position in variable 'pos'</pre> | |
| 64 | <pre>delay(15); // tell servo to go to position in variable pos delay(15); // waits 15ms for the servo to reach the position</pre> | |
| 66 | 1 // waits 15ms for the serve to reach the position | |
| 67 | <pre>for(j = 180; j >= 1; j -= 1) { // goes from 180 degrees to 0 degrees</pre> | |
| 68 | <pre>myservo.write(j); // tell servo to go to position in variable 'pos'</pre> | |
| 69 | delay(15); // waits 15ms for the servo to reach the position | |
| 70 | } | |
| 71 | | |
| 72 | | - |
| × | 117 | |
| | 87 Col1 Sel0 3.33 KB ANSI LF INS C/C++ Source Code | |

