





AVR Basics	
AVR's contain SRAM, EEPROM, External SRAM interface, ADC, Hardware Multiplier, UART, USART, etc. A <i>tinyAVR</i> and a <i>megaAVR</i> stripped off all the peripheral modules, leaves us with the AVR Core — the same for all AVRs.	
 Datasheets are complete technical documents — a reference on how a given peripheral/feature works. 	
•1. One Page—Key information and Feature List •2. Architectural Overview •3. Peripheral Descriptions	
4. Memory Programming 5. Characteristics	
 6. Register Summary 7. Instruction Set Summary 8. Packaging Information 	
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			Re	gist	er N	lame	2S		UNIVERS FLOR	TY OF IDA
He car	re is 1 see	the tha	regi t se	ister rial (sum comr	imary nunic	for t ation	he US uses	ART. 7 re	Wo gs
،	1 for	Dat	а							
ب	1 for	stat	tus							
	3 for 2 for egister Si	con bau	trol Id ra	ate						
1.16 H	Register De	escriptio	n - USAF	RT				0		
1.16 H	Register De	escriptio Bit 7	n - USAF Bit 6	RT Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0	Page
1.16.1 Address	Register De Name DATA	Bit 7	n - USAF Bit 6	RT Bit 5	Bit 4	Bit 3 DATA[7:0]	Bit 2	Bit 1	Bit 0	Page 249
1.16.1 Address -0x00 -0x01	Register De Name DATA STATUS	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3 DATA(7:0] BUFOVF	Bit 2 PERR	Bit 1	Bit 0	Page 249 249
1.16.1 Address +0x00 +0x01 +0x02	Register Do Name DATA STATUS Reserved	Bit 7 RXCIF	Bit 6	Bit 5	Bit 4	Bit 3 DATA(7:0] BUFOVF	Bit 2 PERR	Bit 1	Bit 0	Page 249 249
21.16 H 21.16.1 Address -0x00 -0x01 -0x02 -0x02 -0x09	Register D Name DATA STATUS Reserved CTRLA	Bit 7 RXCIF	Bit 6	Bit 5 DREIF	Bit 4 FERR LVL(1:0)	Bit 3 DATA[7:0] BUFOVF TXCIN	Bit 2 PERR	Bit 1	Bit 0 RXB8	Page 249 249 251
21.10 H 21.16.1 Address -0x00 -0x01 -0x02 -0x03 -0x04	Register Do Name DATA STATUS Reserved CTRLA CTRLB	Bit 7 RXCIF	Bit 6 TXCIF	Bit 5 DREIF RXCIN	Bit 4 FERR LVL[1:0] RXEN	Bit 3 DATA(7:0] BUFOVF TXCIN TXEN	Bit 2 PERR TLVL[1:0] CLK2X	Bit 1	Bit 0 RXB8 - - - - - - - - - - - - -	Page 249 249 251 251
21.10 H 21.16.1 Address -0x00 -0x01 -0x02 -0x03 -0x04 -0x05	Register Do Name DATA STATUS Reserved CTRLA CTRLB CTRLC	Bit 7 RXCIF - - - - -	n - USAF Bit 6 TXCIF - - -	RT Bit 5 DREIF - RXCIN - PMO	Bit 4 FERR ILVL[1:0] RXEN 2E[1:0]	Bit 3 DATA(7:0) BUFOVF TXCIN TXEN SBMODE	Bit 2 PERR TLVL[1:0] CLK2X	Bit 1	Bit 0 RXB8 - VU[1:0] TXB8	Page 249 249 251 251 253
21.10 H 21.16.1 Address -0x00 -0x01 -0x02 -0x03 -0x04 -0x05 -0x06	Register Do Name DATA STATUS Reserved CTRLA CTRLB CTRLB CTRLC BAUDCTRLA	Bit 7 RXCIF	n - USAF Bit 6 TXCIF - - -	Bit 5 DREIF RXCIN PMO	Bit 4 FERR LVL[1:0] RXEN 2E[1:0]	Bit 3 DATA[7:0] BUFOVF TXCIN TXEN SEMODE BSEL[7:0]	Bit 2 PERR TLVL[1:0] CLK2X	Bit 1	Bit 0 PXG8 - VL[1:0] TXB6	Page 249 249 251 251 253 255











	Assembly Code Sample	FLOR	SITY OF
anntantantantantantantantantantantantant	<pre> 1 /* 2 * Test_DAQ_ASM.asm 3 * Created: 01/10/2015 7:07:13 PM 4 * Author: Arroyo 5 */ 6 * 7 * 7 * 7 * 7 * 7 * 7 * 7 * 7 * 7 * 7</pre>	ler * * * * * * * * * * * * * * * * * * *	
annun hudur. A	Assembly language source file length : 3308 lines : 90 Ln : 1 Col : 1 Sel : 0 0 Dos/Window	/s UTF-8	IN5
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	Assemb	ly Code San	nple 🎯 🕅	VERSITY OF ORIDA
3	1 LDI ZL, LOW (PORTD	OUT) ; Z> PORTD_OUT		^
3	2 LDI YH, HIGH (PORT	C_OUT)		
3	3 LDI YL, LOW (PORTC	_OUT) ; Y> PORTC_OUT		
3	Idi K20, 0X03	;Set PDU & PDI direction as Out	put	
	6 1di P20 0v01	Set DC0 direction as Output		
3	7 STS PORTC DIR. R20	, see rev arreetton as output		
3	8			
3	9 ; Set PDO pin High &	PD1 Low		
4	0 1di R20, 0x01	;Set PD0 High & PD1 Low		
4	1 ST Z, R20			
4	2 Idi R20, 0x00	;Set PC0 High		
4	3 ST Y, R20	;Turn on Debug LED (Active Low)		
4	4 Loop:	;Delay Loop 1		
4	dec R19	; Decrement R19		
	o Drne Loop 7 Idi P19 Over	Postore 255 in P10	abei	
A Annuntuntur a	a dec P18	Decrement P18		
	9 brne Loop	:If not zero jump to the Loop 1	abel	
N/ 5	0 1di R18, 0xFF	Restore 255 in R18		
5 N	1 1di R19, 0xFF	;Restore 255 in R19		
¹² 5	2 dec R17	;Decrement R17		
5	3 brne Loop	;If not zero jump to the Loop 1	abel	
5	4			
	5 ;Set Set PD0 pin Low	& PD1 Hign		
	o 1d1 R17, 0x05	Store 05 in R17		
	Idi Ris, OxFF	Store OXFF in R18		
	9 1di 820, 0x02	Set PDO Low & PD1 High (P20)		
	0 ST Z. R20	, the new a the negli (Rev)		~
S	mbly language source file length : 33	08 lines: 90 lo: 9 Col: 74 Sel: 010	Dos\Windows UTE-8	INS
	may anyways avails file length (35	we make we	Postering of Post	112
///////////////////////////////////////				
/ / 1				
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[61 62	ldi R20, 0x01 ST Y, R20	:Turn off Debug LED (R20) :Turn off Debug LED (Active Low)	^
	64	Loop2:	:Delay Loop 2	
	65	dec R19	;Decrement R19	
	66	brne Loop2	; If not zero jump to the Loop label	
	67	ldi R19, OxFF	;Restore 255 in R19	
	68	dec R18	;Decrement R18	
	69	brne Loop2	; If not zero jump to the Loop label	
	73	Idi R16, OXFF	Restore 255 in P19	
	72	dec B17	Decrement R17	
	73	brne Loop2	; If not zero jump to the Loop label	
	74			
	75	;Repeat Count times		
	76	1di R17, 0x05	;Restore 05 to R17	
1hunhun/		ldi R18, OxFF	Restore 255 to R18	
	78	Idi R19, OxFF	Restore 255 to R19	
N/	20	101 R20, 0X01	;Set PDO High & PDI LOW	
	81	ldi B20. 0x00	:Set PCO High	
	82	ST Y, R20	;Set PCO High	
	83	dec R16	;Decrement COUNT	
	84	brne Loop	; If not zero jump to the Loop label	
	85	128218231 2112		
	86	1di R20, 0x03	:Set PDO High & PD1 High to end	
	87	ST Z, RZU	ATTACK OF Debug IPD (Lating Iou)	
	89	ST Y. R20	Fruch on bebug Lab (Accive Low)	
	90	Here: rimp Here		



の名前の	Writing C Programs
	#includes
	main(){
	Initialize LCD, Servos, PWM, A/D, Serial,
	DIR, variables
	while(1){
	read sensors
dunhunhunhunhunhunhunhunhunhunhunhunhunhu	interpret sensors
ruluu A	function1()
	update LCD } // while
	沂// end of main
S Sammin	
aduuluuluuluuluulu	function1(args) {
University of Florida, EEL 5666 © Dr. A. Antonio Arroyo	stuff } // end of function 1



の一部で	V	/riting C I	Programs		IVERSITY OF
anduntantantanta ang	16 * In: 17 * AV 18 * / 19 #incl: 20 #incl: 21 #incl: 23 Eintm 24 // b 25 C 26 P 27 P 28 in 30 31 32 33 34 35 37 36 37 38	<pre>clude header files for all & Software Framework (ASF) dde <arf.h> dde <arf.h> dde <uti delay.h=""> dde <uti delay.h=""> de <uti delay.h=""> de <uti>delay_me(1000); FORTD_OUT = 0x01; PORTD_OUT = 0x01; PORTD_OUT = 0x01; PORTD_OUT = 0x02; _delay_ms(1000);</uti></uti></uti></uti></uti></uti></uti></uti></uti></uti></arf.h></arf.h></pre>	<pre>drivers that have been i</pre>	<pre>imported from ion for DAQ PDO & PDI link LEDs on. The led is ac on PDO, off LED o off. The led is a 0 on PDO, on LED o</pre>	tive low n PD1 ctive low n PD1
53	40 P 41 P 42 B 43 44 J 45 J	DRTC.OUTCLR = 0x01; // DRTD_OUT = 0x03; // while(1) { // // TODO Code that runs	Turns the debug led on. turn on External LEDs on main Loop forever in the robot	The led is active PDO, and LED on P	low Dl
<i>mpmp</i> mpmp	C source file	length : 1630 lines : 45	Ln:1 Col:1 Sel:0]0	Dos\Windows UTF-8	INS
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	Arduino Programs
1	<pre>#include <liquidcrystal.h></liquidcrystal.h></pre>
2 3 4	/* EEL-5666 Example 1: Use of Digital Output Pins. Turn on/off external LEDs connected to Pins 11 & 12 alternatively & the internal LED on ENG 13 co(off 144 active) as corb externa (1 active)
6 7 8	Arroyo August 26 2015
9 10 11	int extLED = 11; // External LED connected to digital pin 11 int extLED = 12; // External LED connected to digital pin 12 int intLED = 13; // Internal LED connected to digital pin 13
17 13 14 15 16	<pre>void setup() (pinMode(extLED, OUTPUT); // sets digital pin 12 as output pinMode(extLED, OUTPUT); // sets digital pin 12 as output pinMode(intLED, OUTPUT); // sets digital pin 13 as output</pre>
10 19 20	<pre>int count=0; void loop() (if (count < 1) (</pre>
	<pre>for (int j=0; j<8; j++) { digitalWrite(13, HIGN); // set the internal LED on digitalWrite(12, LGW); // set the external LED1 off digitalWrite(11, HIGN); // set the external LED2 on</pre>
26 27 28 29	<pre>delay(500); // wait for a 1/2 second digitalWrite(13, LOW); // set the internal LED off digitalWrite(14, HIGH); // set the external LED on digitalWrite(11, LOW); // set the external LED off</pre>
30 31 32 33	delay(500): // wait for a 1/2 second } digitelWrite(13, HICH): // set the internal LED on digitelWrite(12, HICH): // set the external LED on
S 51 34 36 37	<pre>digitalWrite(11, HIGH); // set the external LED2 on) count=1; }</pre>
//////////////////////////////////////	text file length : 1403 lines : 37 Ln : 1 Col: 27 Sel : 0 0 UNIX UTF-8 INS
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